

Model Name: GA-B85M-D3H

www.xinxunwei.com 400-800-9990

SHEET TITLE Revision 1.2

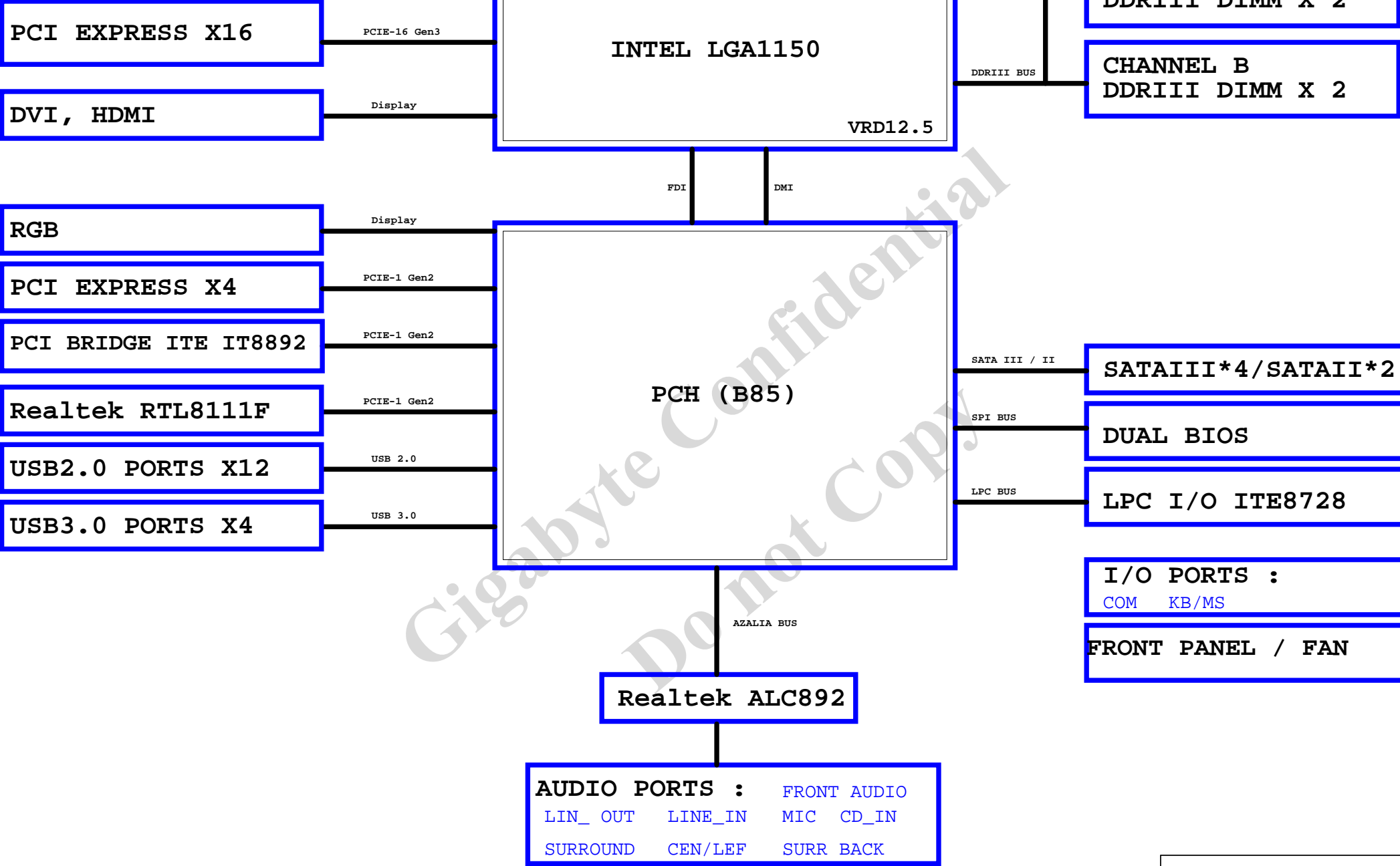
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI SLOT1,2
17	ITE 8728 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV,-PROCHOT
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC892-GR
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX , CLOCK GEN, TPM
27	VCORE ISL95820_1

SHEET TITLE

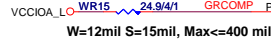
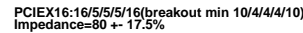
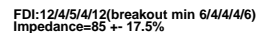
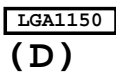
28	VCORE ISL95820_2
29	RT8120_DDR POWER
30	LPT, M3 POWER
31	DVI, HDMI
32	IT8892E

Gigabyte Technology		
Title		
Cover Sheet		
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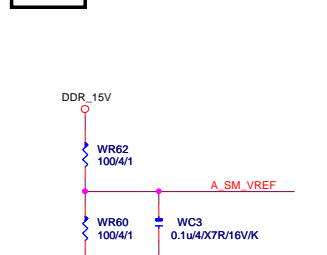
BLOCK DIAGRAM



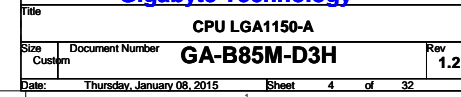
(E)

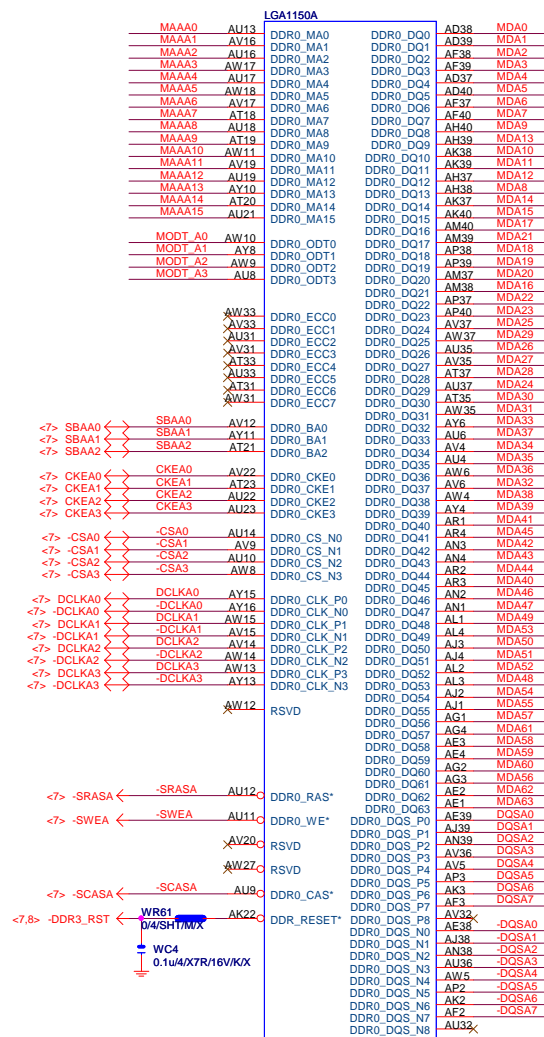


CPU SVID

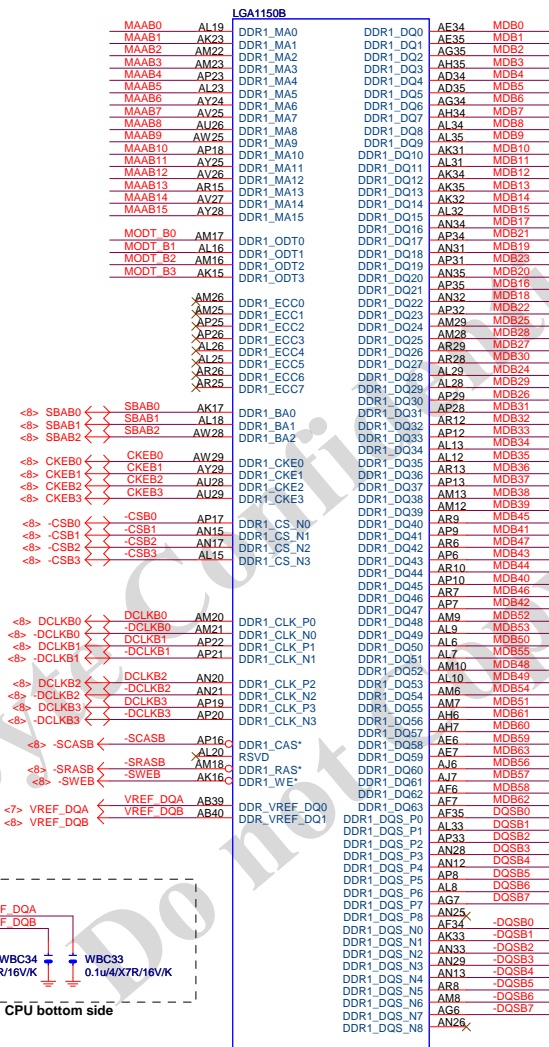


THRMTRIP DISABLE

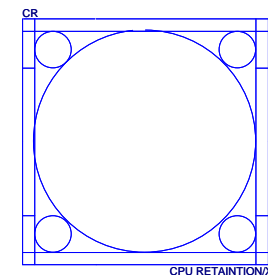




HASWELL/10SC1-F01150-11R_10SC1-F01150-12R



HASWELL/10SC1-F01150-11R_10SC1-F01150-12R

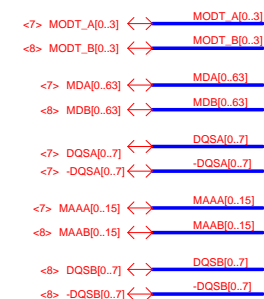


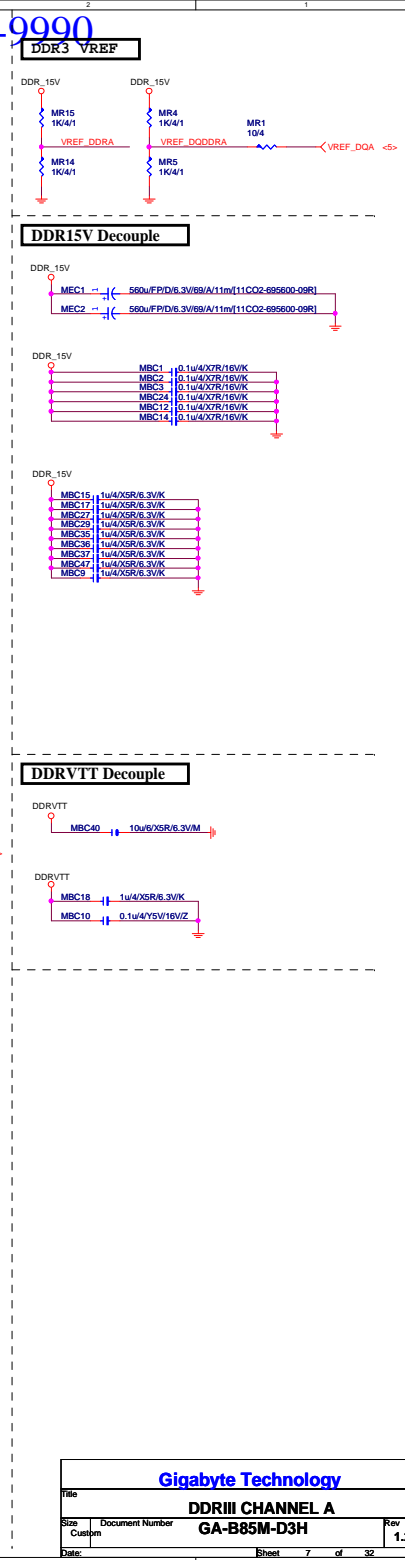
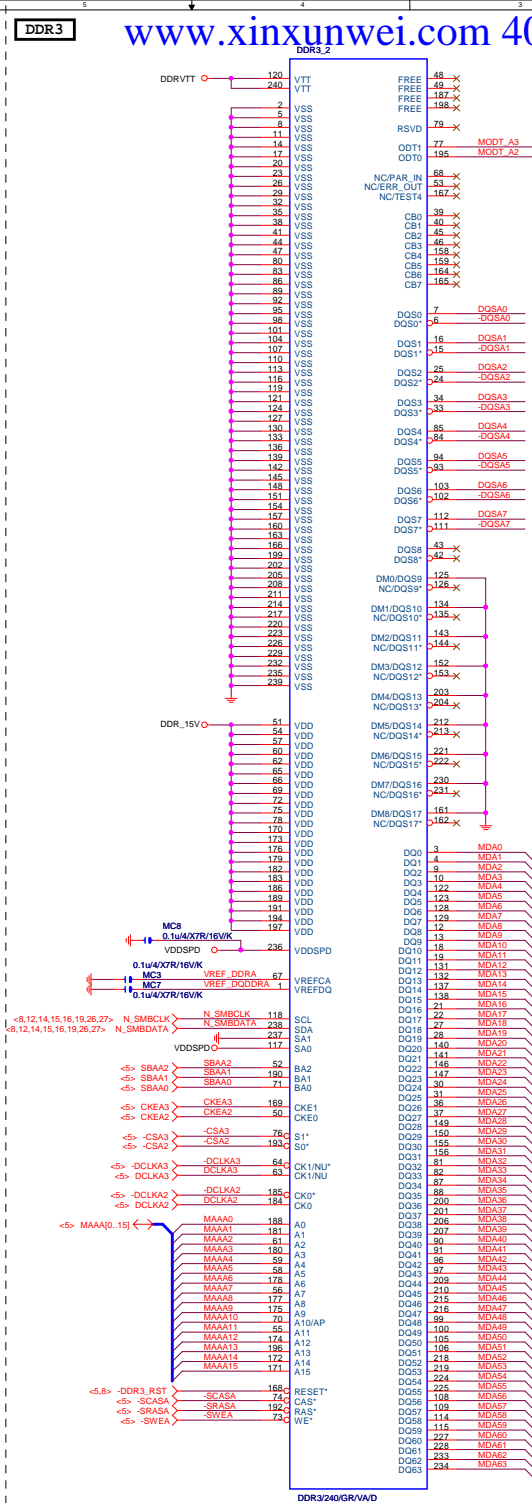
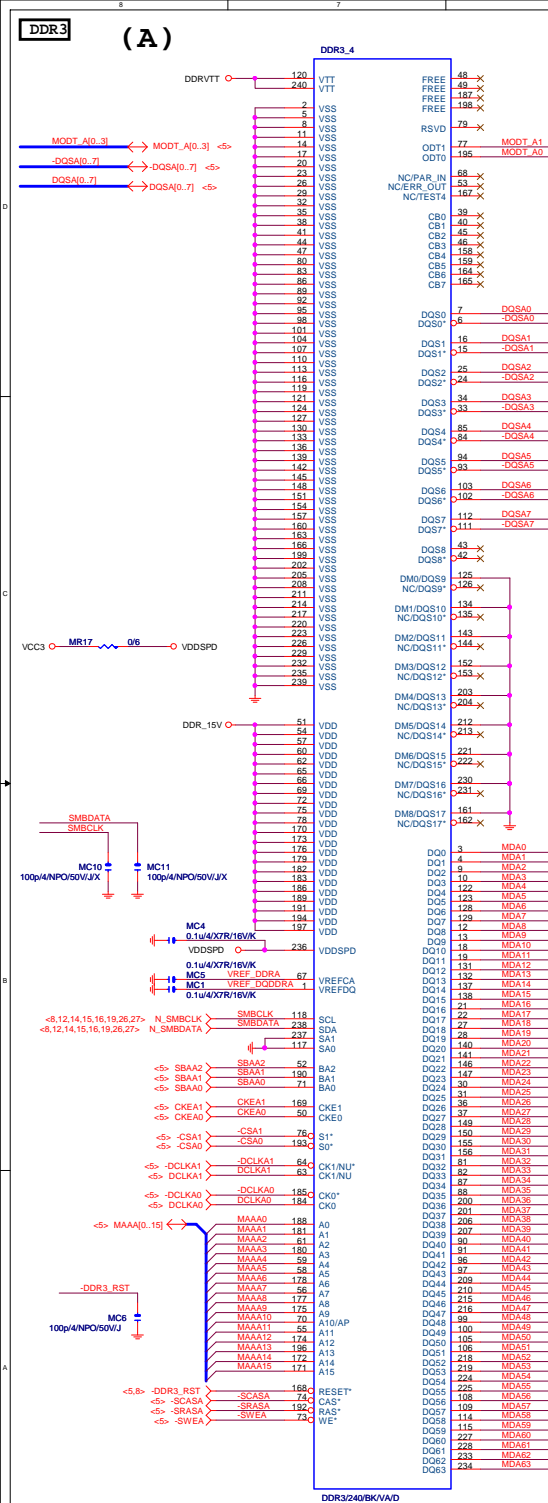
LGA1150_P

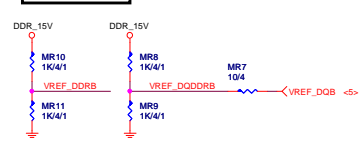
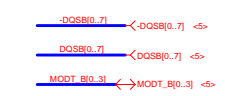


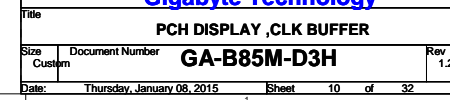
ILM_BP/1156/CSP/ILM_BP/1156/CSP/12KRC-0F0001-52R_12KRC-0F0001-51R

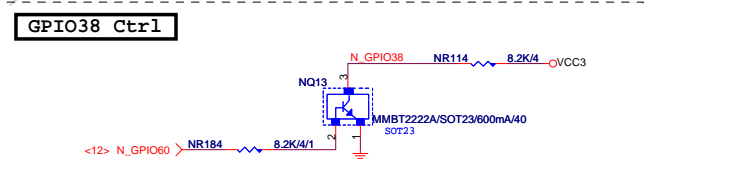
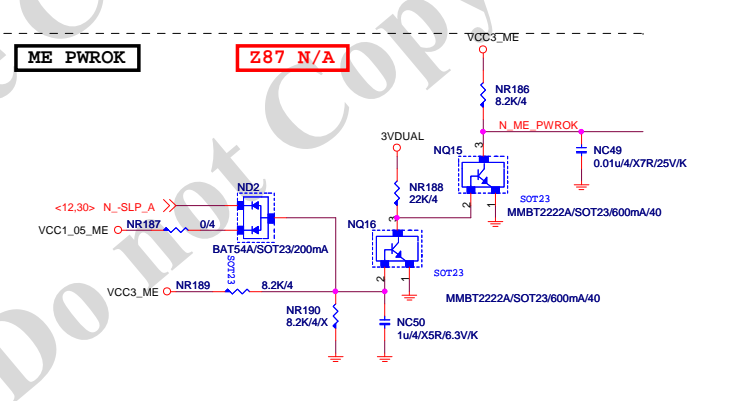
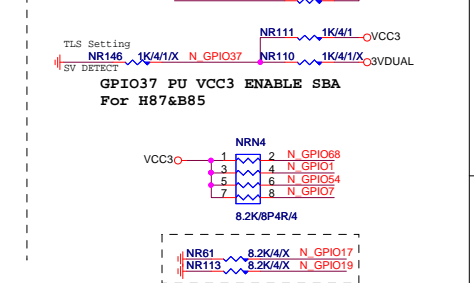
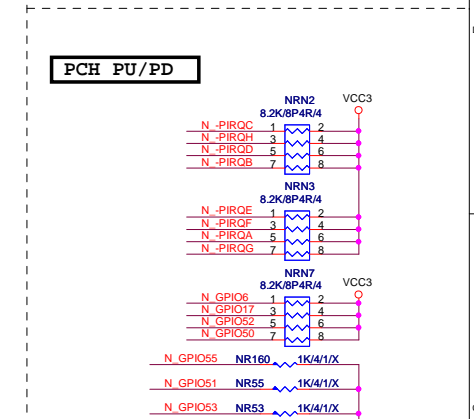
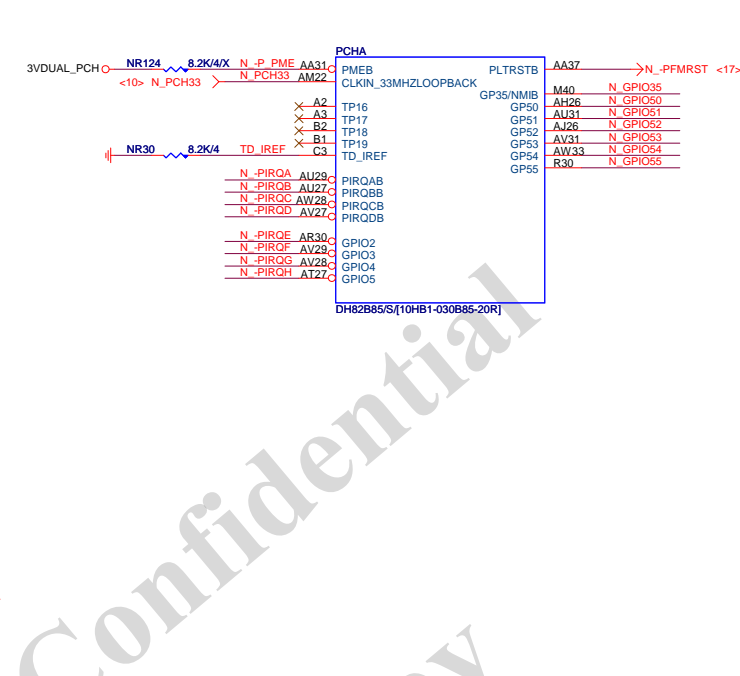
DDR BUS





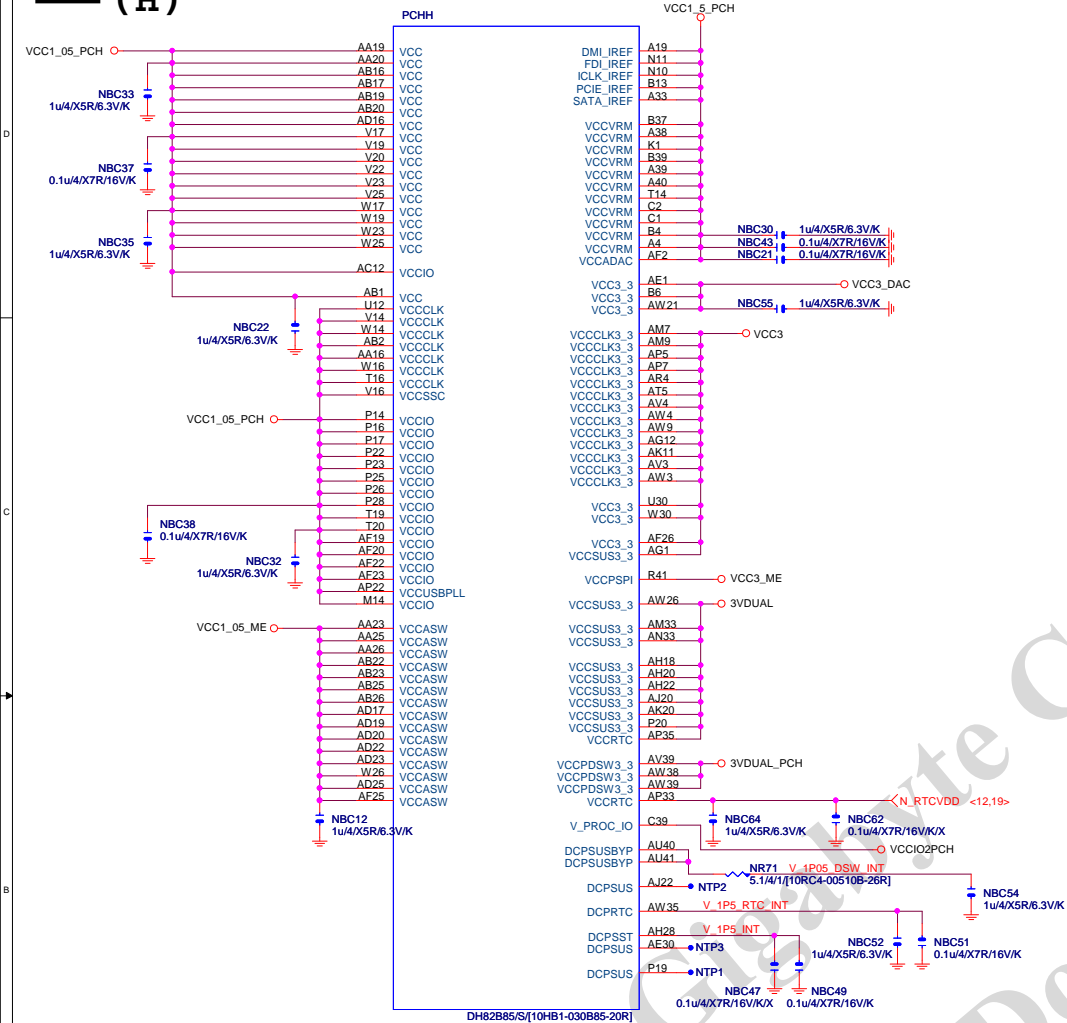






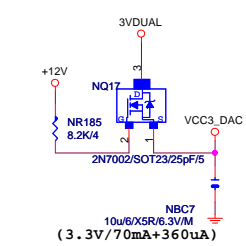


PCH (H)

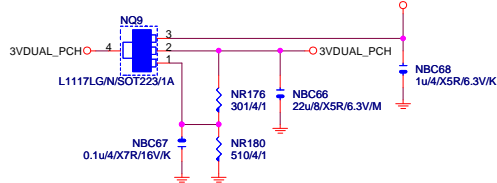


VCC3_DAC

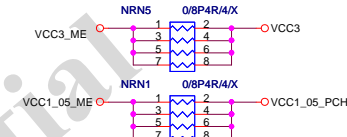
CLOSE北橋(注意震盪水波紋)

 $(3.3V/70mA+360\mu A)$ 

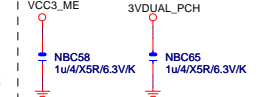
3VDUAL_PCH



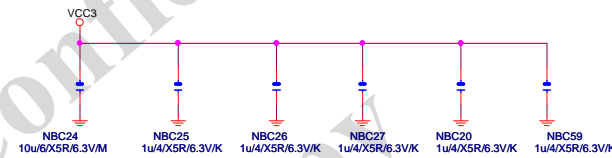
SHT PWR



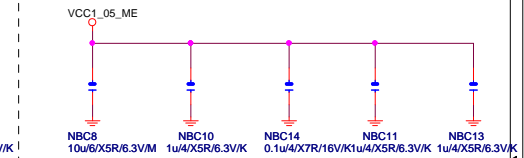
CAP



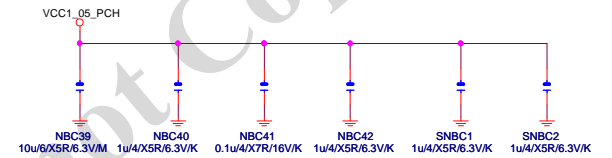
(3.3V) (X6)



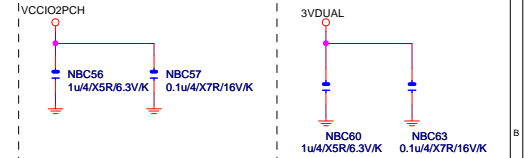
(1.05V) (X5)



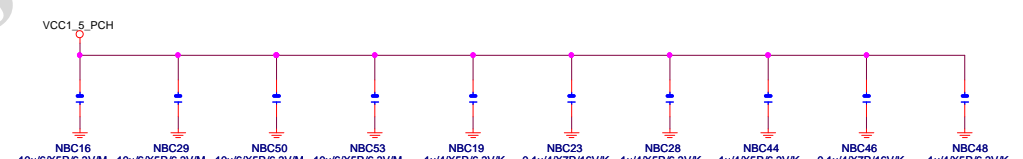
(1.05V) (X6)



(1.05V)(X2) (3.3V) (X2)

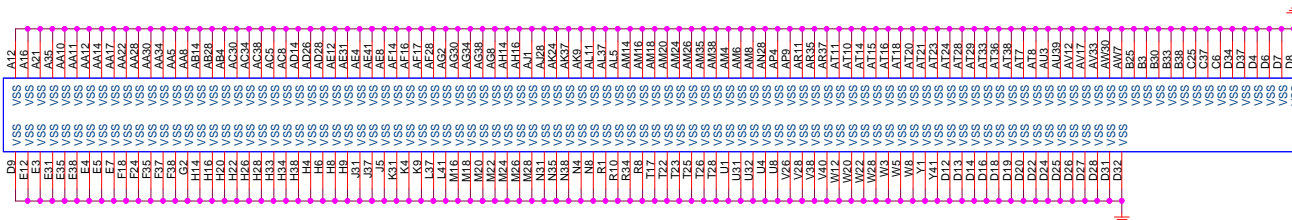


(1.05V) (x10)



PCH

(I)



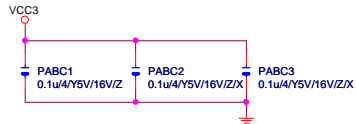
PCHI
DH82B85/S/[10HB1-030B85-20R]

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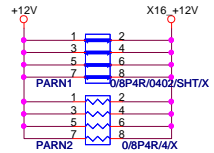
PCH PWR ,GND

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PCIEX16 CAP



PCIEX16 PROTECT SHT

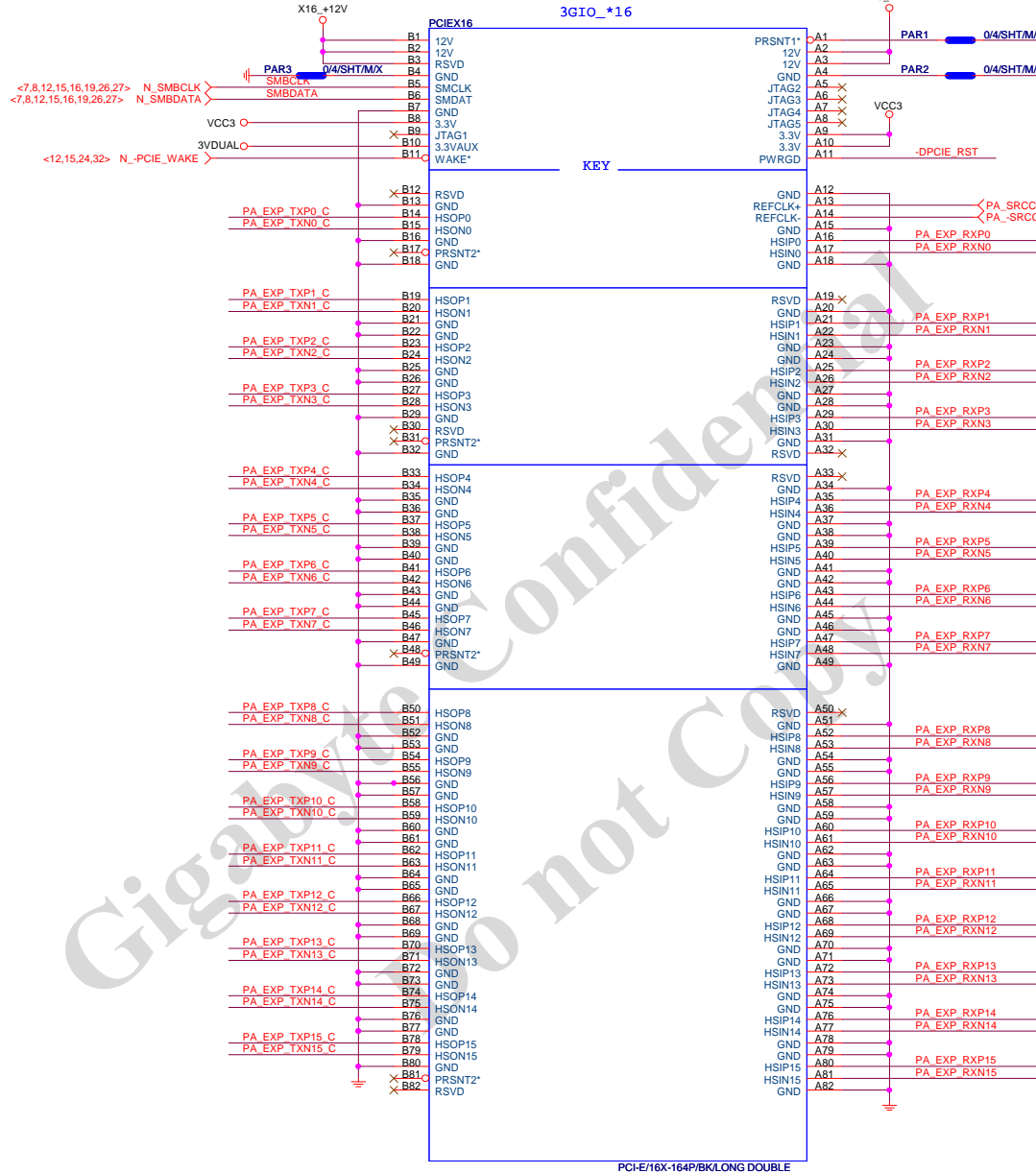


PCIEX16 AC CAP

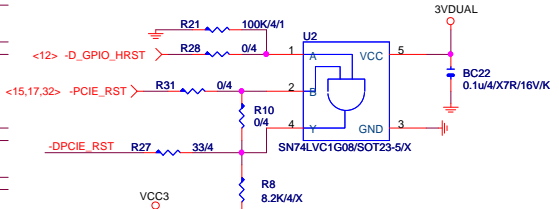
PA EXP TXP0	PAC5	0.22u4/X5R6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R6.3V/K	PA EXP TXN15 C

PA EXP RXP[0..15] >>> PA_EXP_RXP[0..15] <4>
PA EXP RXN[0..15] >>> PA_EXP_RXN[0..15] <4>
PA EXP TXP[0..15] >>> PA_EXP_TXP[0..15] <4>
PA EXP TXN[0..15] >>> PA_EXP_TXN[0..15] <4>

PCIEX16 SLOT



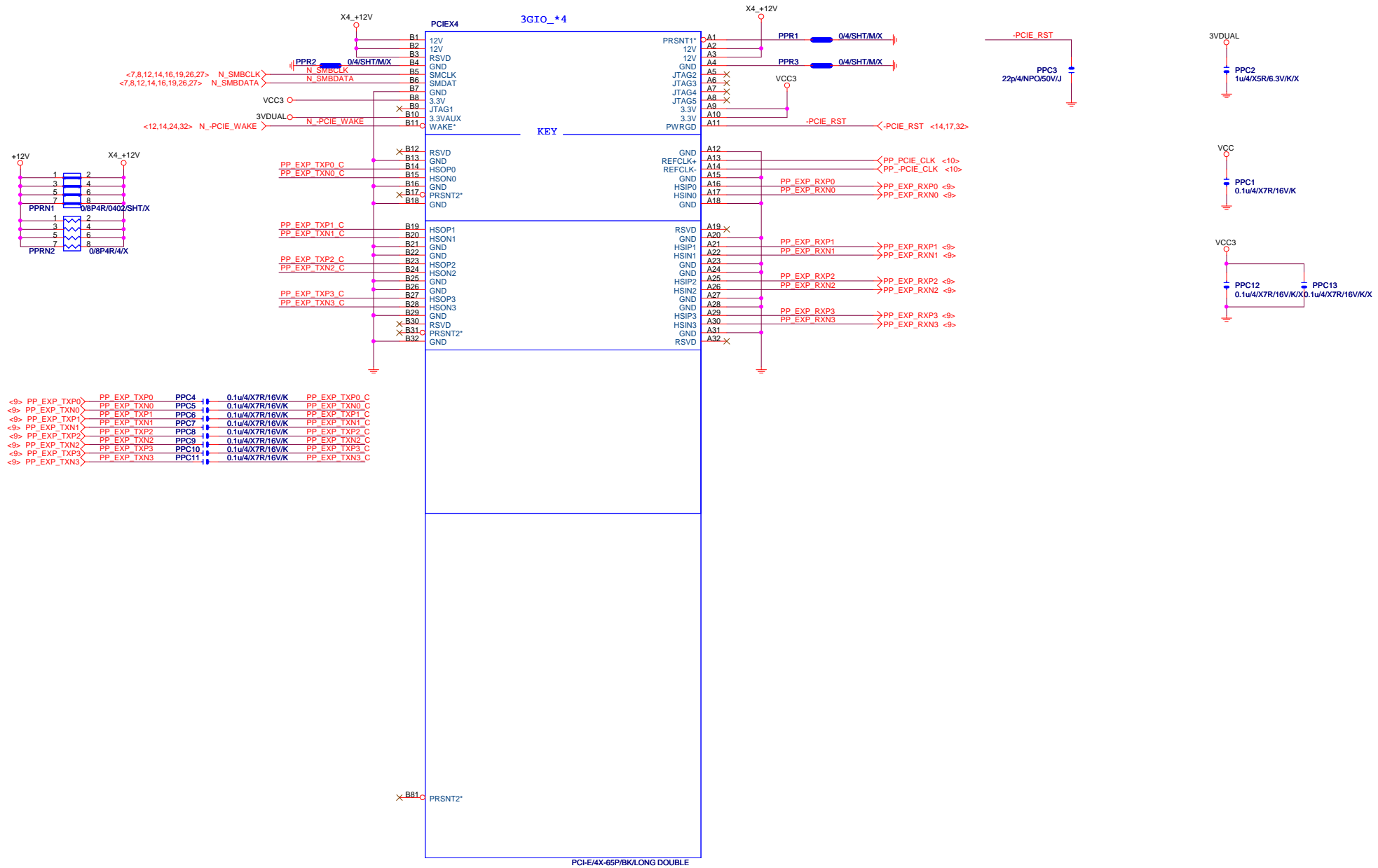
The auxiliary reset circuit is only required for PCIe Gen3 margining and functional link training



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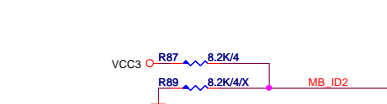
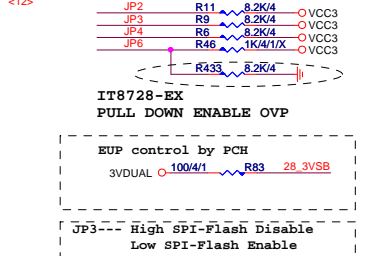
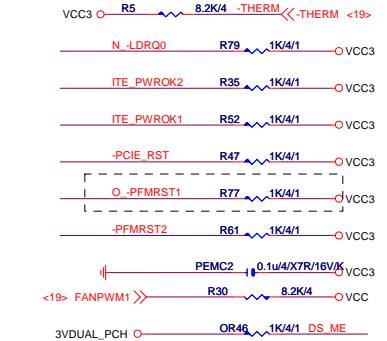
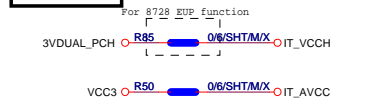
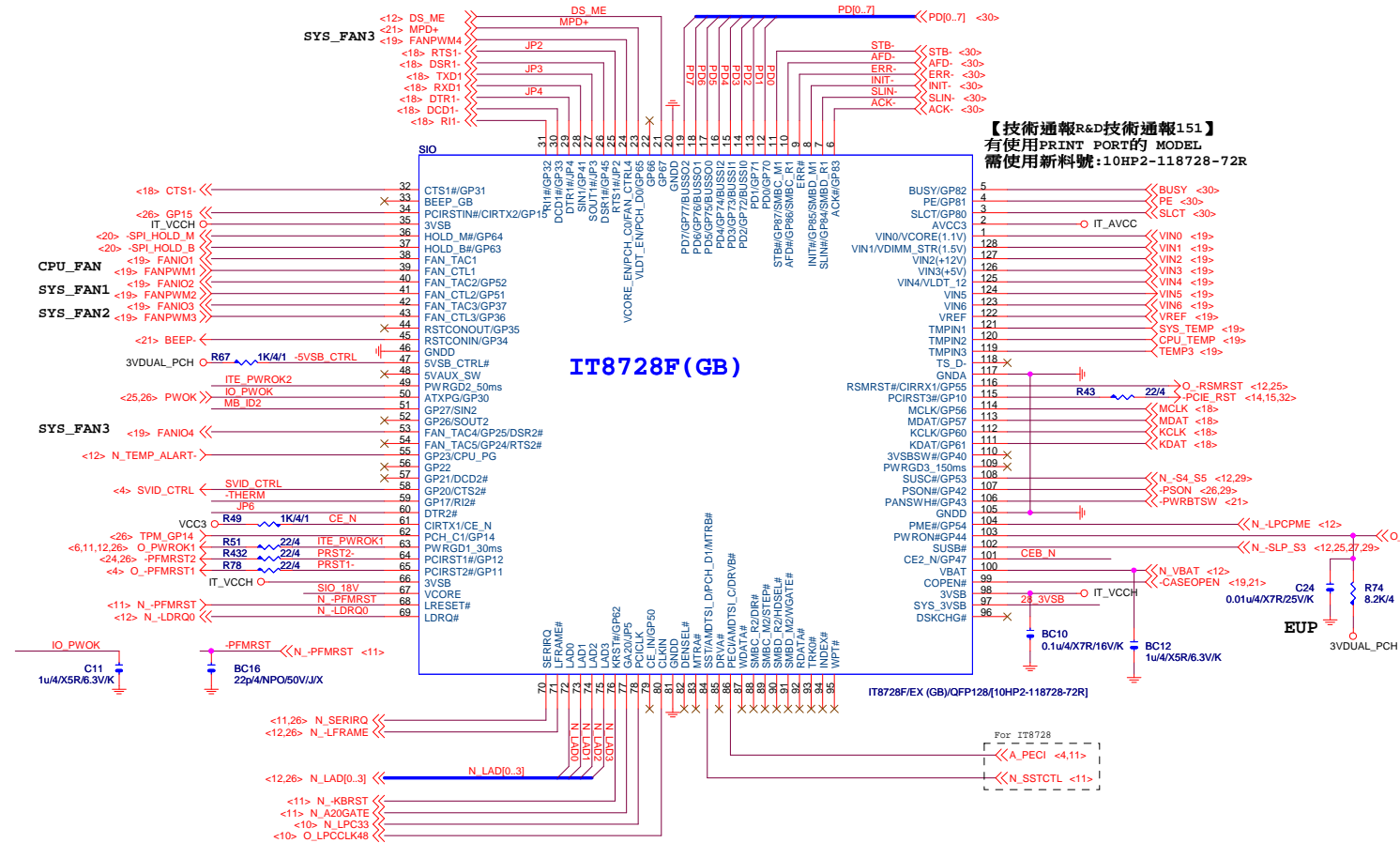
Title			
PCI EXPRESS * 16			
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1.2			
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PCIEX4 SLOT



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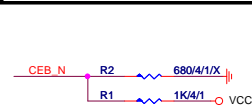
Title			
PCI EXPRESS X 1 PORT			
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IT8728F NOTE

	IT8728
PIN121	VCORE_EN/PCH_C0
PIN120	VLDI_EN/PCH_D0
PIN19	ATXPG
PIN31	PCH_C1
PIN53	SST/AMDTSLI_D/MTRB# /PCH_D1
PIN55	PECI/AMDTSLI_C/DRV#
PIN66	SYS_3VSB
PIN70	GP47
PIN95	VIN2(VCC5)
PIN96	VIN1(VCC12)
PIN97	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0/VCORE(1.1V)/NC

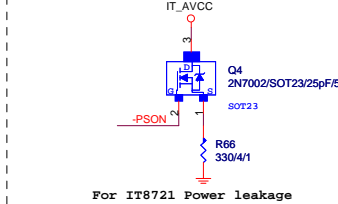
DUAL BIOS OPT STRAP



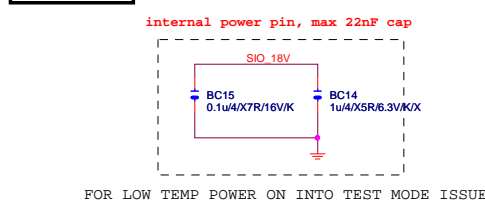
SIO CAP

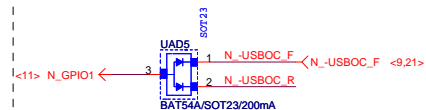
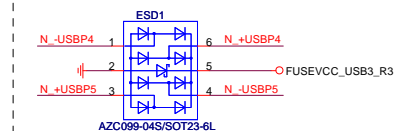
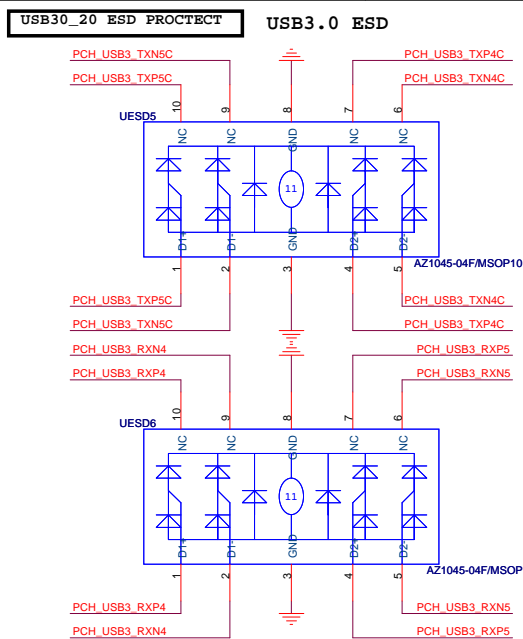
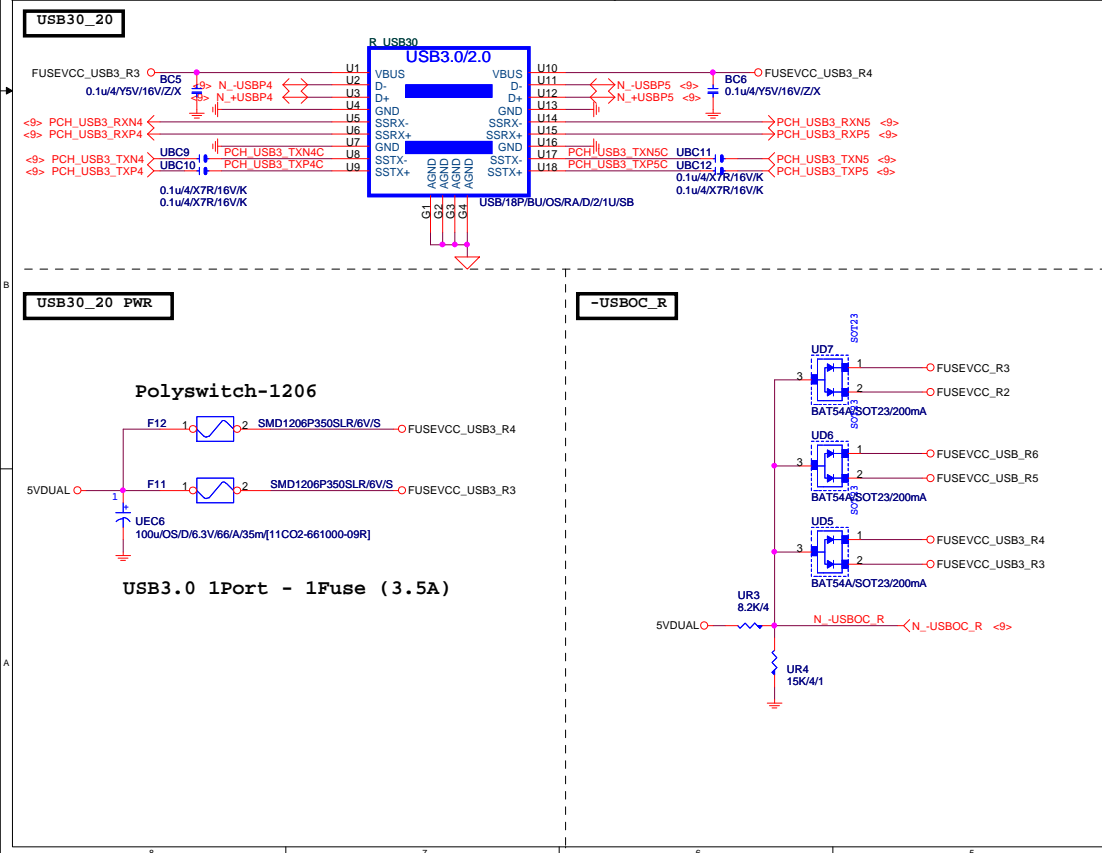
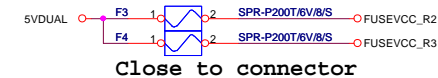
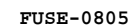
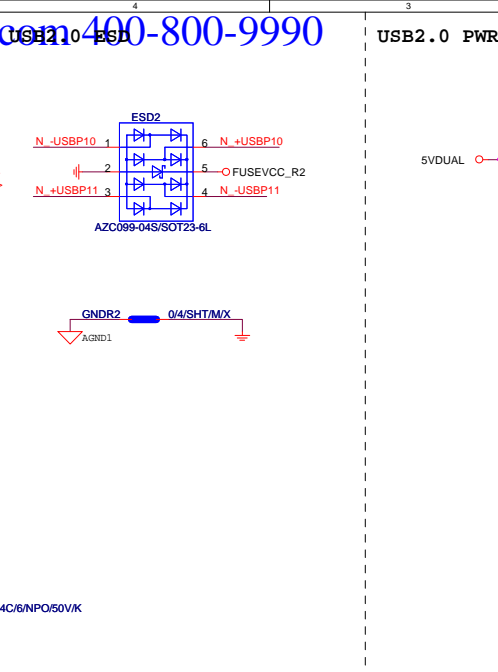
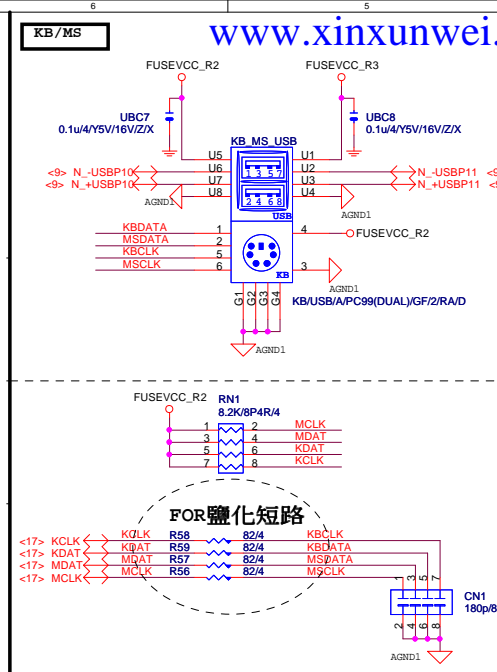
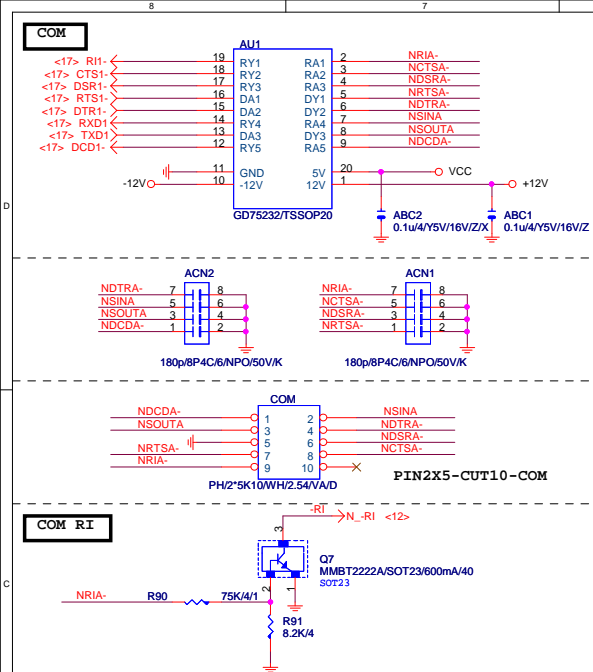


Power leakage

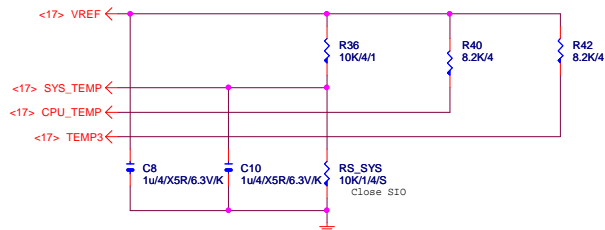


SIO_18V

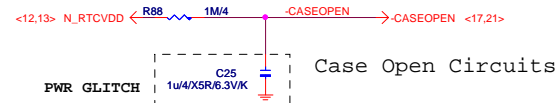




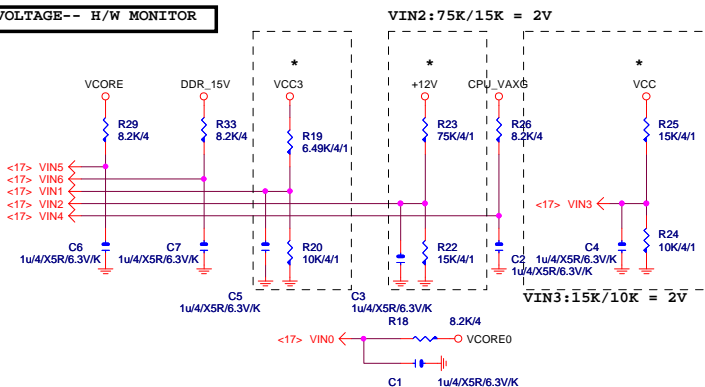
TEMP H/W MONITOR



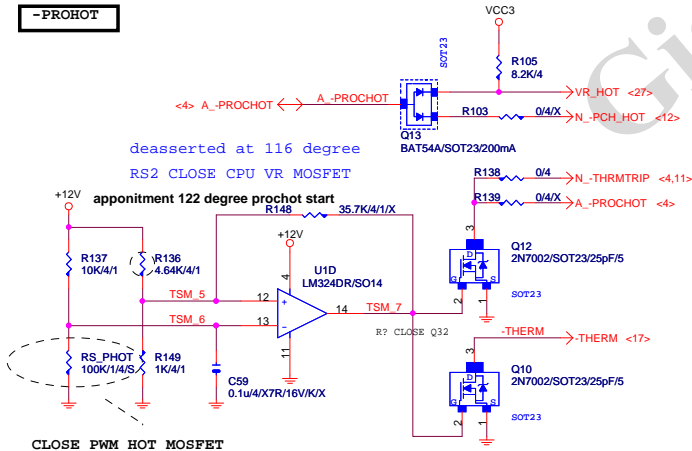
CASE OPEN



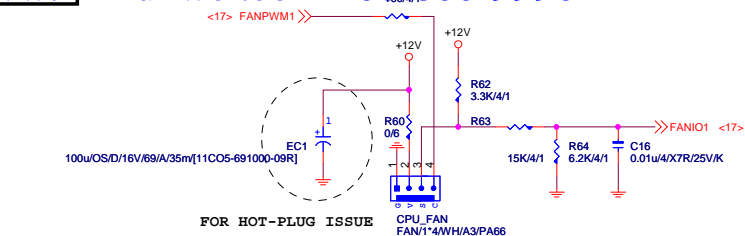
VOLTAGE-- H/W MONITOR



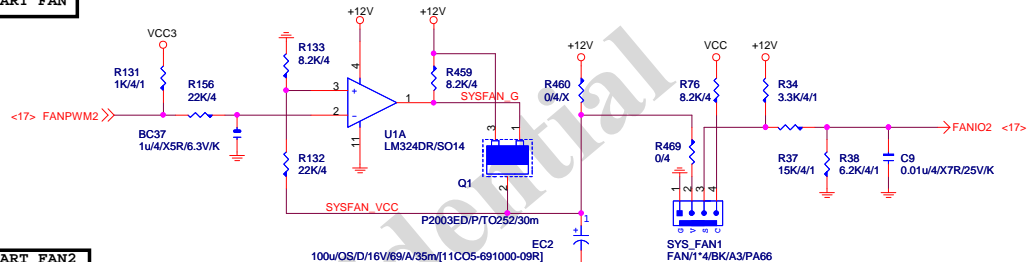
-PROHOT



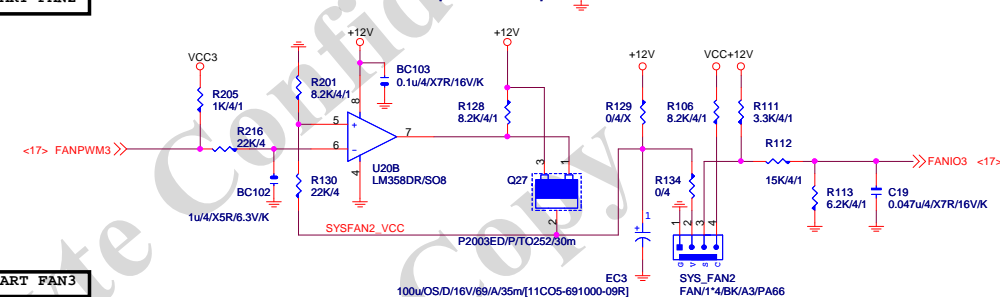
CPU SMART FAN



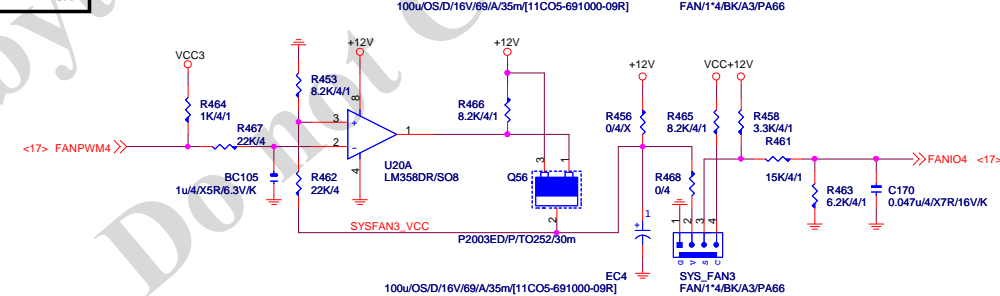
SYS SMART FAN



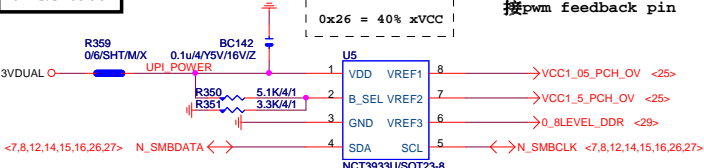
SYS SMART FAN2



SYS SMART FAN3

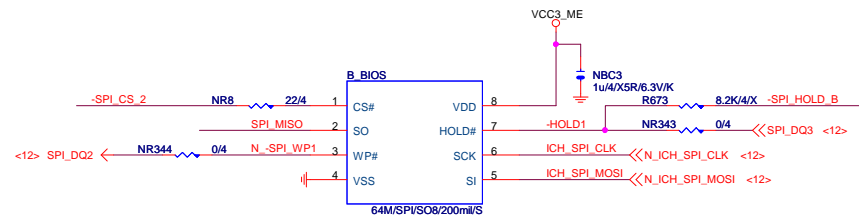
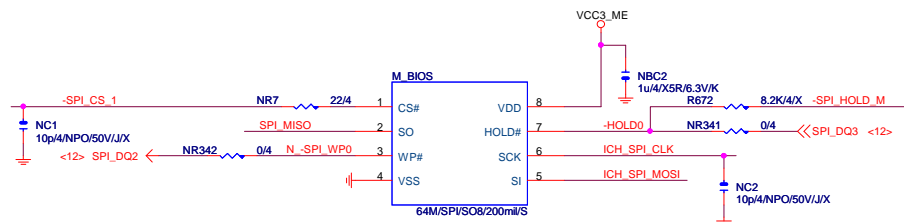


OV NCT3933



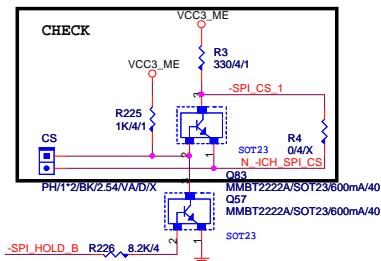
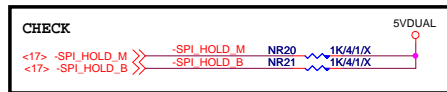
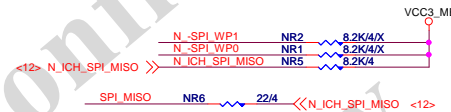
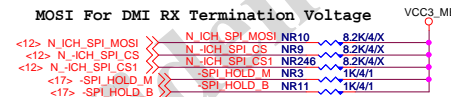
Gigabyte Technology

Title			
HWM,FAN CTRL,OV			
Size	Document Number	Rev	
Custom	GA-B85M-D3H	1.2	
Date:	Thursday, January 08, 2015	Sheet	19 of 32

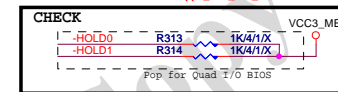
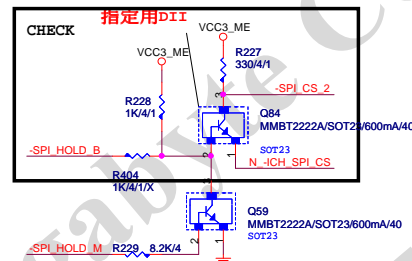


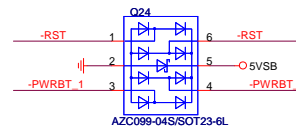
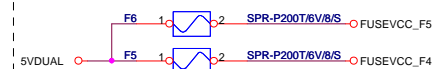
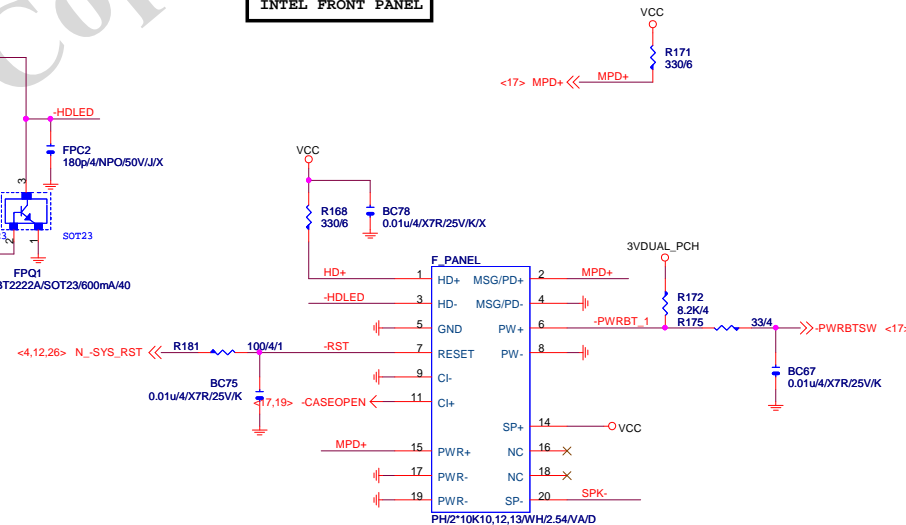
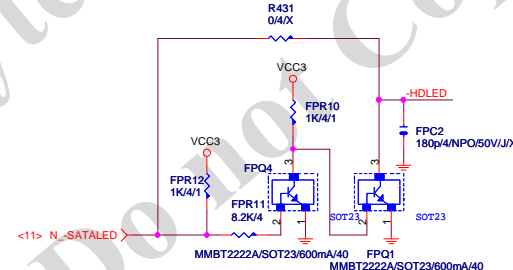
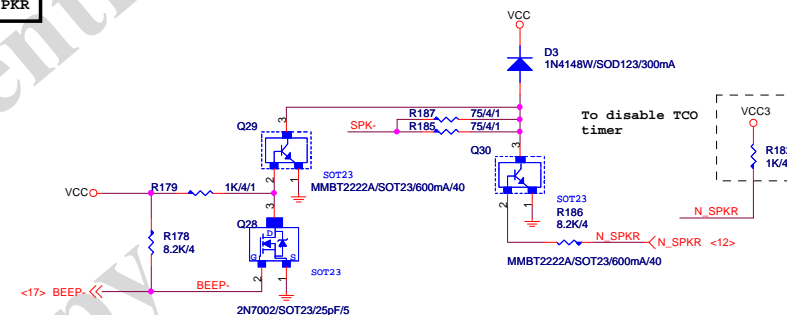
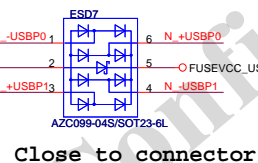
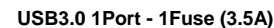
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1k

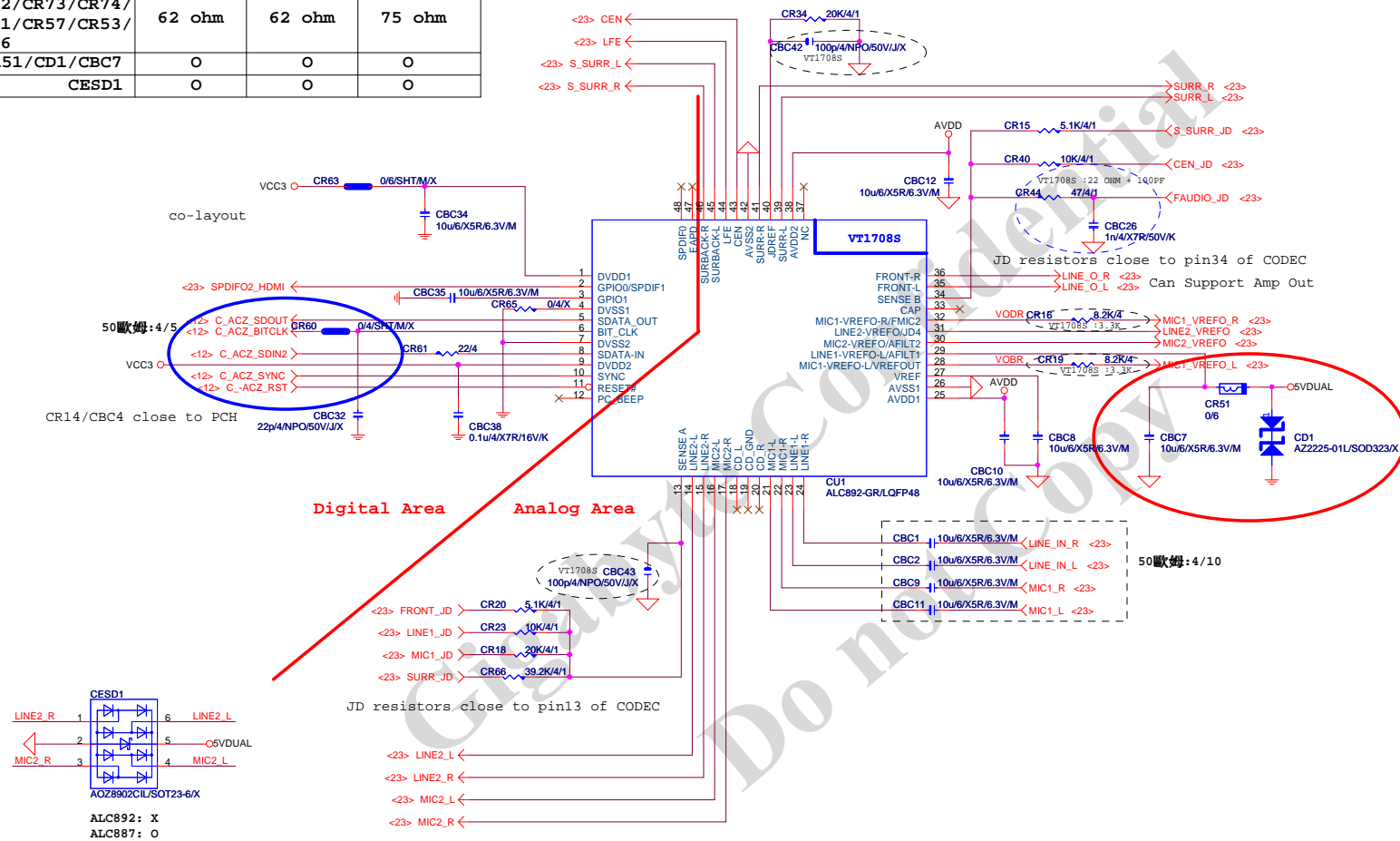


Dual BIOS CS connect
circuit update





	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O



LINE-IN

on

MIC-IN

<<2> MIC1_R <-- CR17 62/4
 <<2> MIC1_L <-- CR22 62/4
 <<2> MIC1_VREF0_L
 <<2> MIC1_VREF0_R
 CBC3 180pF/4NPO/50V/J
 CBC4 180pF/4NPO/50V/J
 AJ_C5
 AJ_C2

SURROUND

CEC10 100nF/D/10V/6*5"[11CE2-651000-05R]
 <22> SURR_R CR73 62/4
 CEC11 100nF/D/10V/6*5"[11CE2-651000-05R]
 <22> SURR_L CR74 62/4
 CR67 22K/4
 CR68 22K/4
 BJ_C5
 BJ_C2
 CB44 180pA/NPO50V/J
 CB45 180pA/NPO50V/J

CEN/LFE

SURR BACK

EMI

CEC14 100uF/D/10V/6.5[11CE2-651000-05R]

CEC15 100uF/D/10V/6.5[11CE2-651000-05R]

CR79 62/4

CR80 62/4

CR71 22K/4

CR72 22K/4

CBC48 180pF/4NPO/50V/J

CBC49 180pF/4NPO/50V/J

<2>S_SURR_R

<2>S_SURR_L

BJ A5

BJ A2

AZALIA FRONT PANEL

AZALIA FRONT PANEL

Diagram illustrating the AZALIA FRONT PANEL circuit, showing various components and connections:

- Power and Biasing:**
 - BAT54A/SOT23/200mA (CQ4) and BAT54A/SOT23/200mA (CQ2) are connected to the circuit.
 - VR1Q8S (3.3K) is connected to the circuit.
- Audio Input/Output:**
 - Line 2 inputs: <22> LINE2_VREFO and <22> MIC2_VREFO.
 - Microphone inputs: <22> MIC2_L and <22> MIC2_R.
 - Audio Jack: <22> AUDIO_JD.
 - Audio Output: F_AUDIO (1-10 pins).
- Resistors and Capacitors:**
 - CR52, CR56, CR10, CR9, CR13, CR14, CR57, CR53, CR12, CR58, CR54, CR55, CR59.
 - Resistor values: 8.2K/4, 22K/4, 20K/4/1, 39.2K/4/1, 0.4K, 180p/4/NPO/50V/J, 100p/4/NPO/50V/J, 100uF/D/10V/6/5[11CE2-65100-05R].
 - Capacitor values: 10u6/XSR/6.3V/M, 10u6/XSR/6.3V/M, 100uF/D/10V/6/5[11CE2-65100-05R].
- Other Components:**
 - VR1Q8S (3.3K) is connected to the circuit.
 - CR58, CR54, CR55, CR59 are connected to the circuit.
 - CR12, CR53, CR13, CR14, CR57, CR58, CR54, CR55, CR59 are connected to the circuit.
 - CR12, CR53, CR13, CR14, CR57, CR58, CR54, CR55, CR59 are connected to the circuit.

Title			
AUDIO JACK			
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SPDIF_OUT

<<2> SPDIF02_HDMI

0/4/SHT/MX

CBC14

100pF/4NPO/50VJ

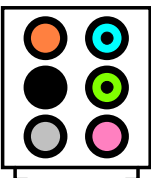
PIN

SPDIF_O

PH1/2/BK/2.5A/VA/D

For HDMI SPDIF

AZALIA JACK



AZALIA JACK

BLUE
LINE-IN

GREEN
LINE-OUT

PINK
MIC-IN

Orange
CEN/LFE

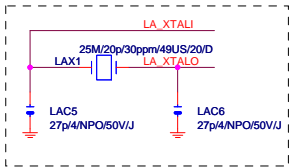
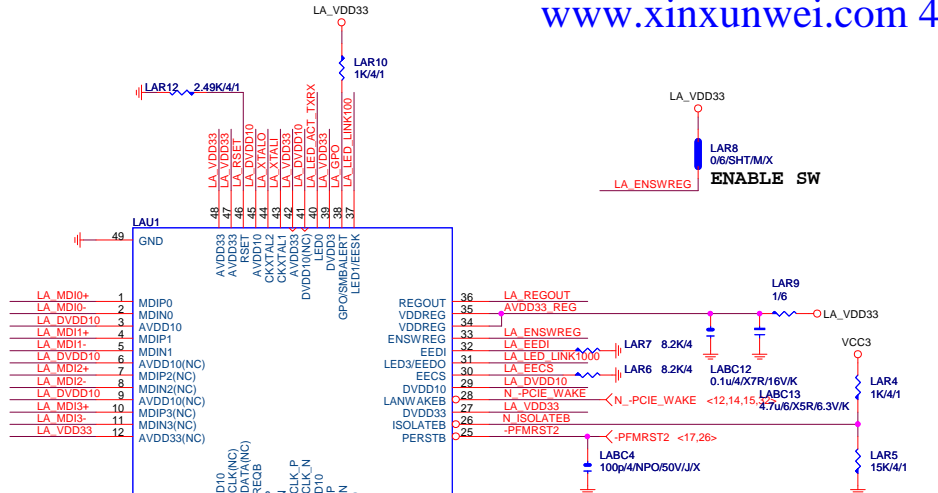
Black
SURROUND

Gray
SURROUND SIDE

P/26P/OR,BK,GY,BU,GE,PK/RA

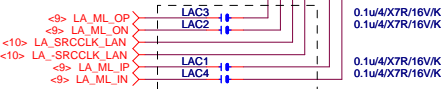
LAN:RTL8111F/VB/VL

www.xinxunwei.com 400-800-9990

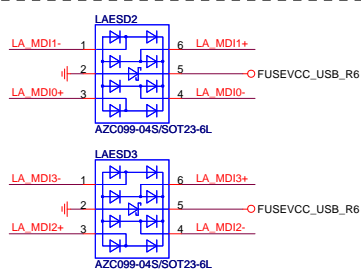


LA_ML-->80歐姆:[15/5/5/5/15]

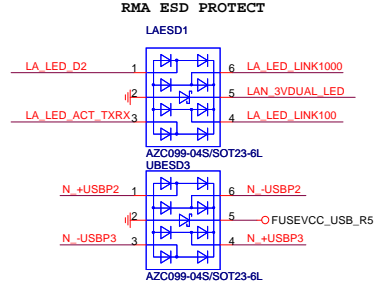
SRCCLK-->50歐姆:[18/4/10/4/18]



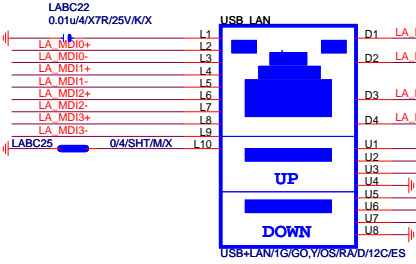
MDI ESD預留28KV *



USB_LAN CONNECTOR



LA_MDI-->100歐姆:[20/4/8/4/20]



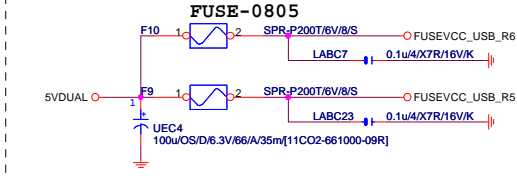
USB_LAN BOM區分:

1. (紅色/12CORE/三倍): USB+LAN/1G/GO,Y/OS/RA/D/1/RED
2. (黑色/12CORE): USB+LAN/1G/GO,Y/OS/RA/D/1
3. (黑色/8CORE): USB+LAN/1G/GO,Y/OS/RA/D/8C

注意:LAN LED PROTECT:(CO-LAYOUT)
1.ESD(6PIN):AOZ8902CIL/SOT23-6(DEFAULT)
2.SURGE(5PIN):AZ2025-04S/SOT23-5L

注意:USB PORT(目前:暫代6,7PORT)
USB-->90歐姆:[15/4.5/7.5/4.5/15]

USB X3 POWER



EMI SHORT PAD

PS:視EMI需求



料號	規格	廠商
11NR6-702009-0ER	1G LAN (12core)	UDE
11NR6-702009-91R	1G LAN (8 core)	FOXCONN
11NR6-702009-92R	1G LAN (8 core)	UDE
11NR6-702009-11R	1G LAN (12core/RED)	UDE
11NR6-702009-12R	1G LAN (8 core/RED)	FOXCONN

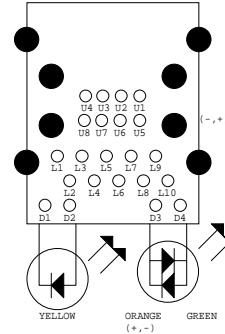
Gigabyte Technology

Realtek RTL8111G

GA-B85M-D3H

Rev 1.2

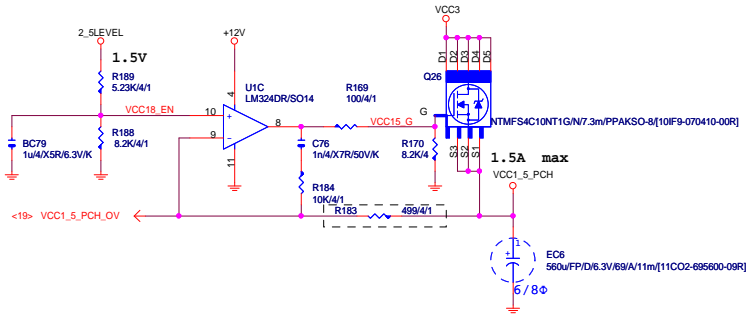
Title	Document Number	Rev
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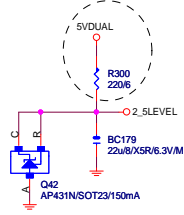
Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

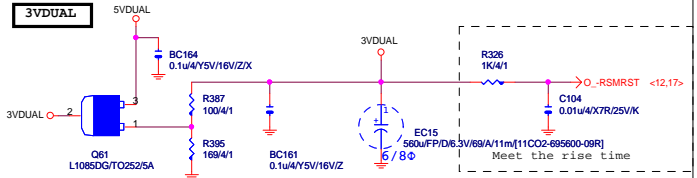
VCC1_8_PCH



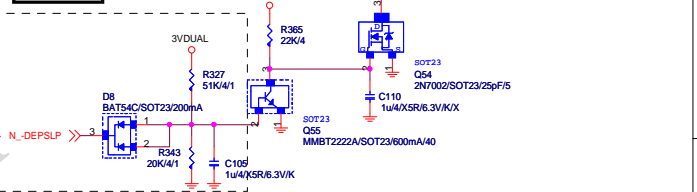
ERP



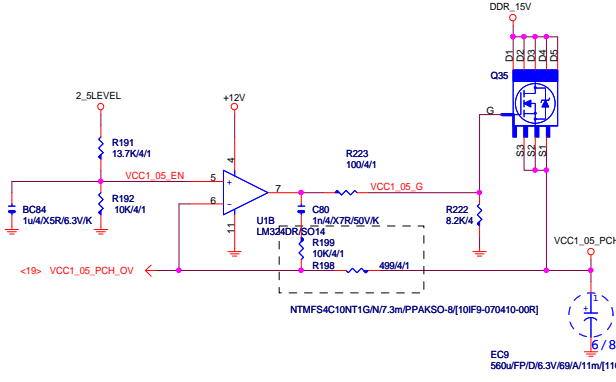
3VDUAL



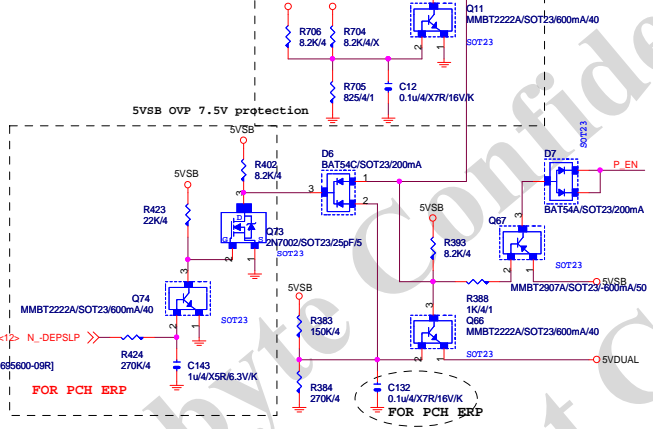
-RSMRST



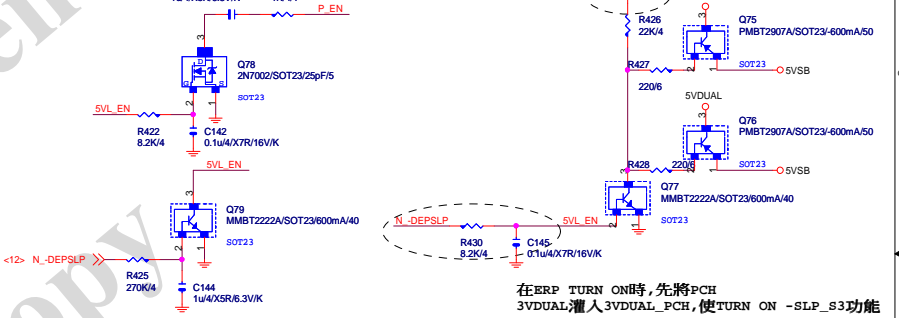
VCC1_05_PCH



5VDUAL SHORT PROTECT

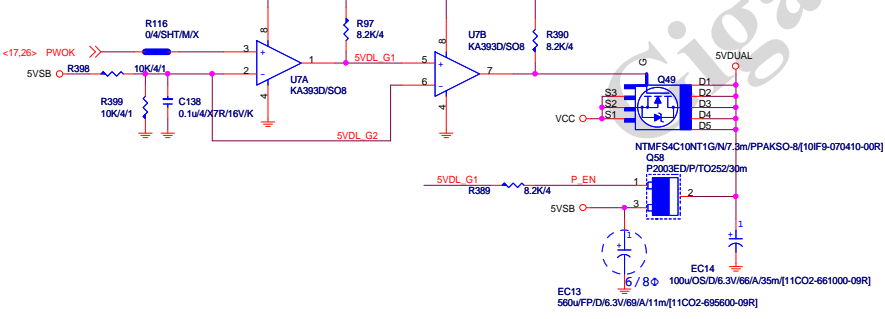


PCH ERP

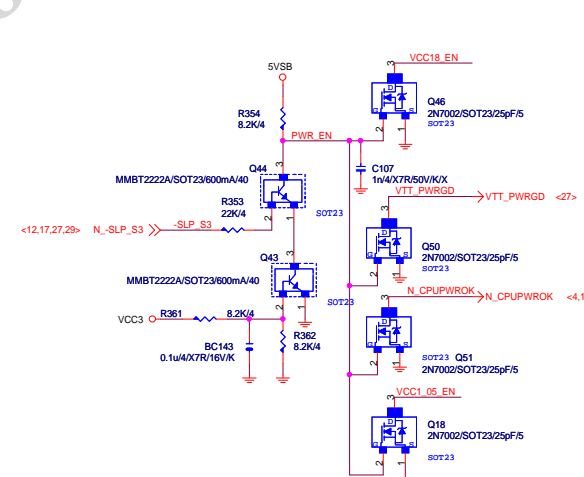


在ERP TURN ON時, 先將PCH 3VDUAL灌入3VDUAL_PCH, 使TURN ON -SLP_S3功能

5VDUAL



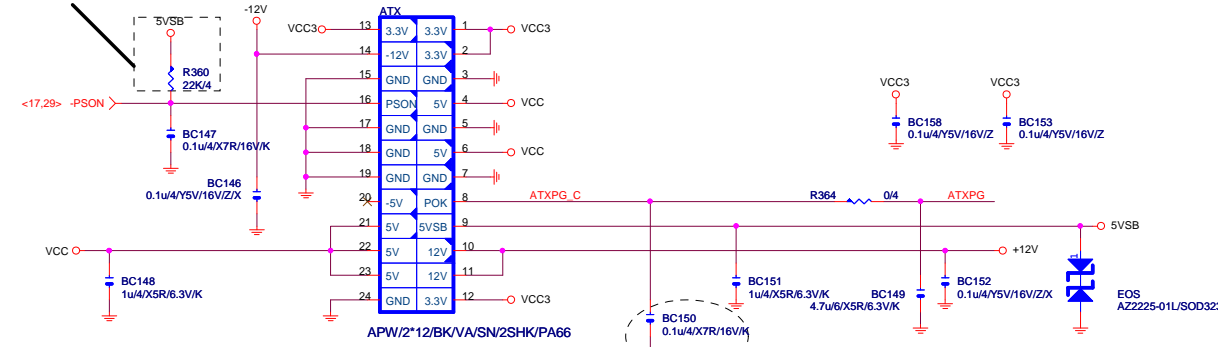
PWR SEQ



Gigabyte Technology			
DISCRETE POWER			
GA-B85M-D3H			
Rev	1.2		
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ATXX24 POWER CONNECTOR

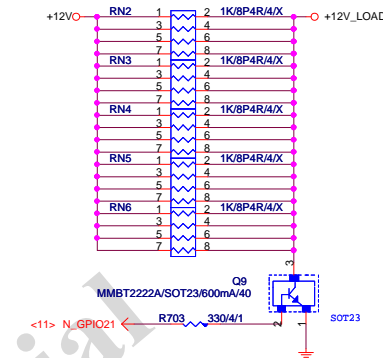
【技術通報R&D技術通報155】



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【技術通報R&D技術通報153】

To fix 12V light load abnormal issue

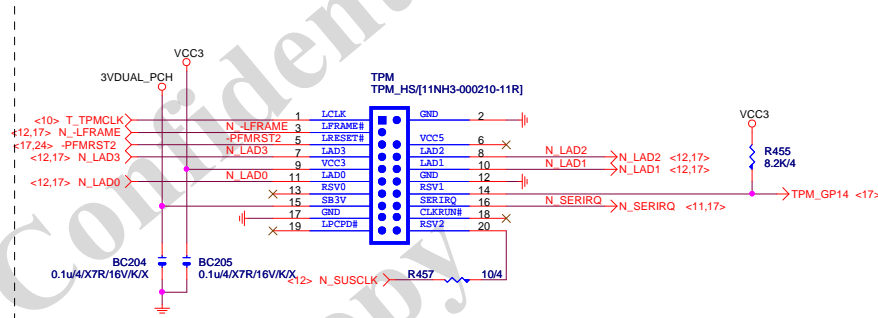


ATXX4 POWER CONNECTOR

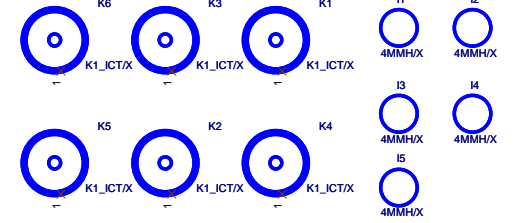
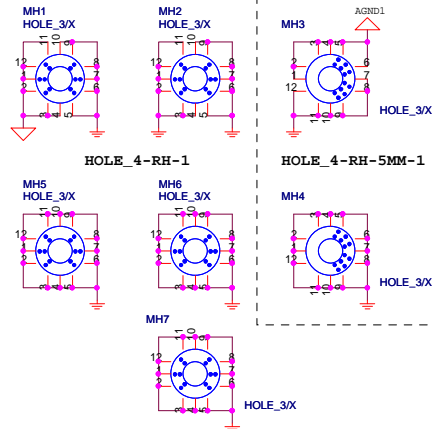
APW/2*4BK/OC/PI4.2/VA/SN/0H:Location ATX_12V_2X4



TPM



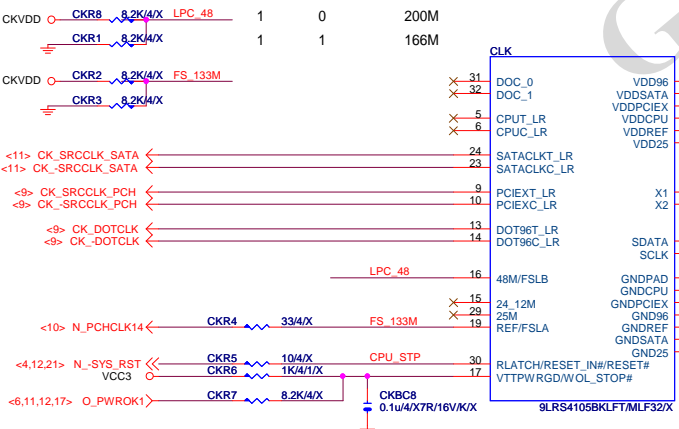
To prevent the 5VSB under loading when boot



CLK GEN

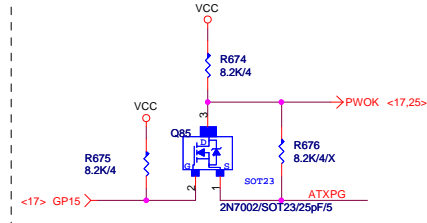
CPU Frequency Selection

FSLB	FSLA	CPU
0	0	100M <Default>
0	1	133M
1	0	200M
1	1	166M



PWOK PATCH

【技術通報R&D技術通報154】



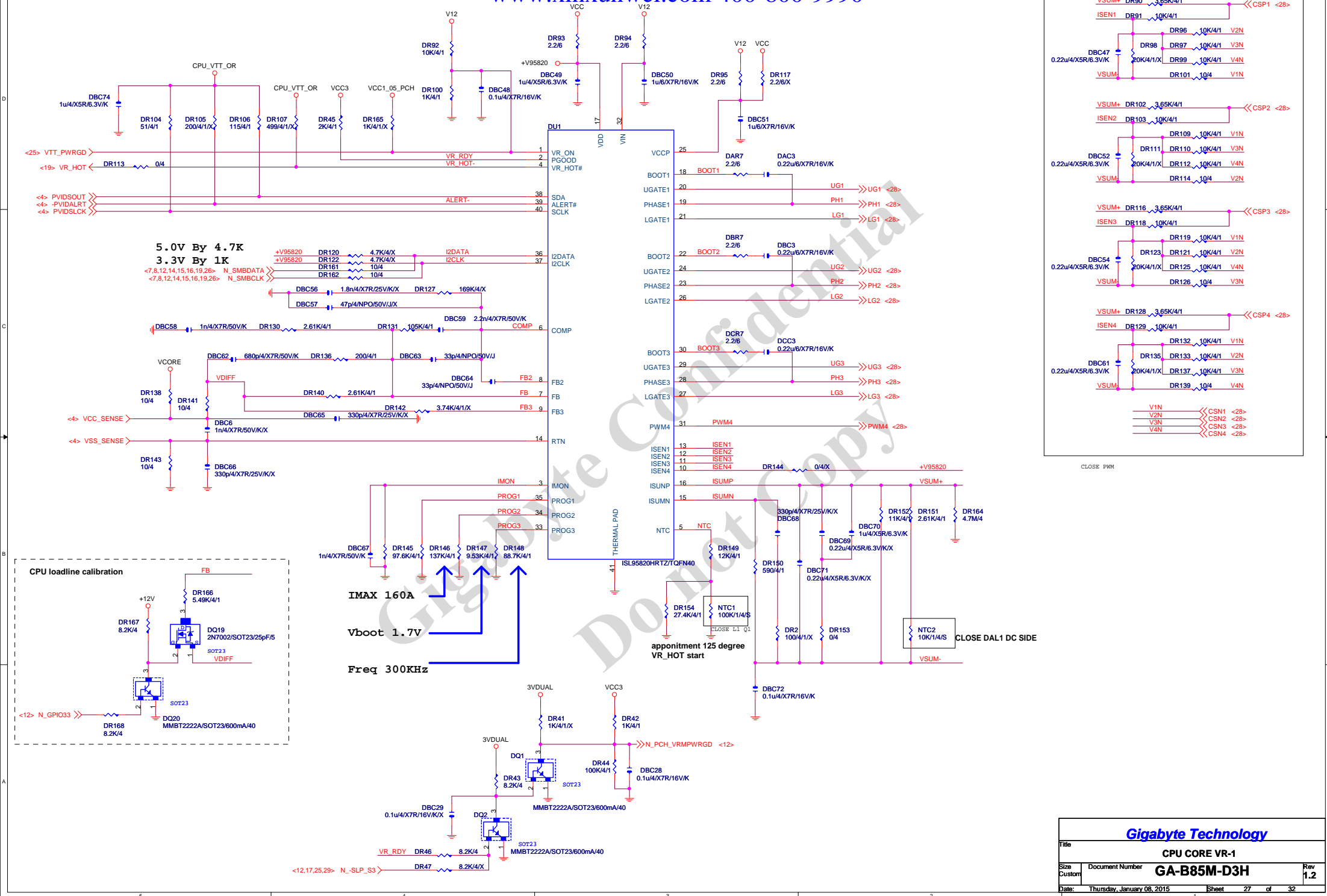
Gigabyte Technology

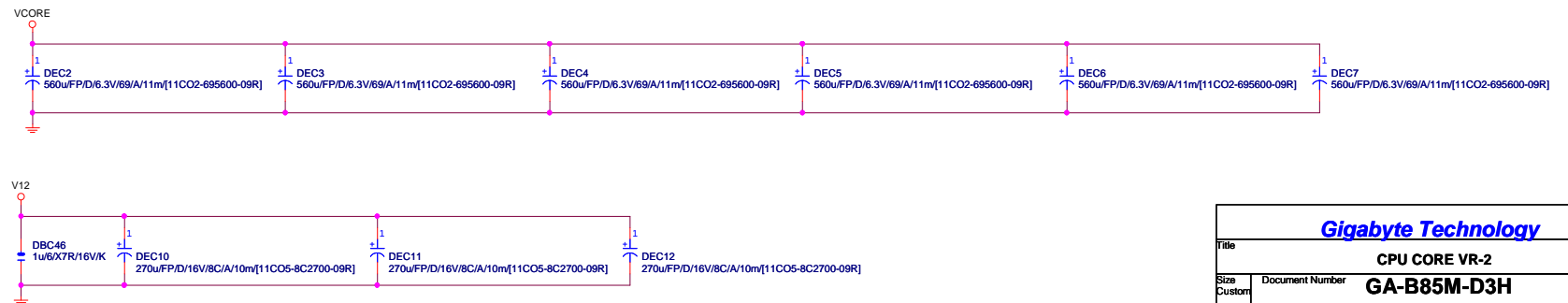
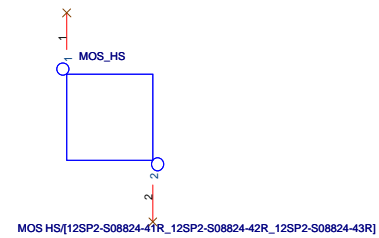
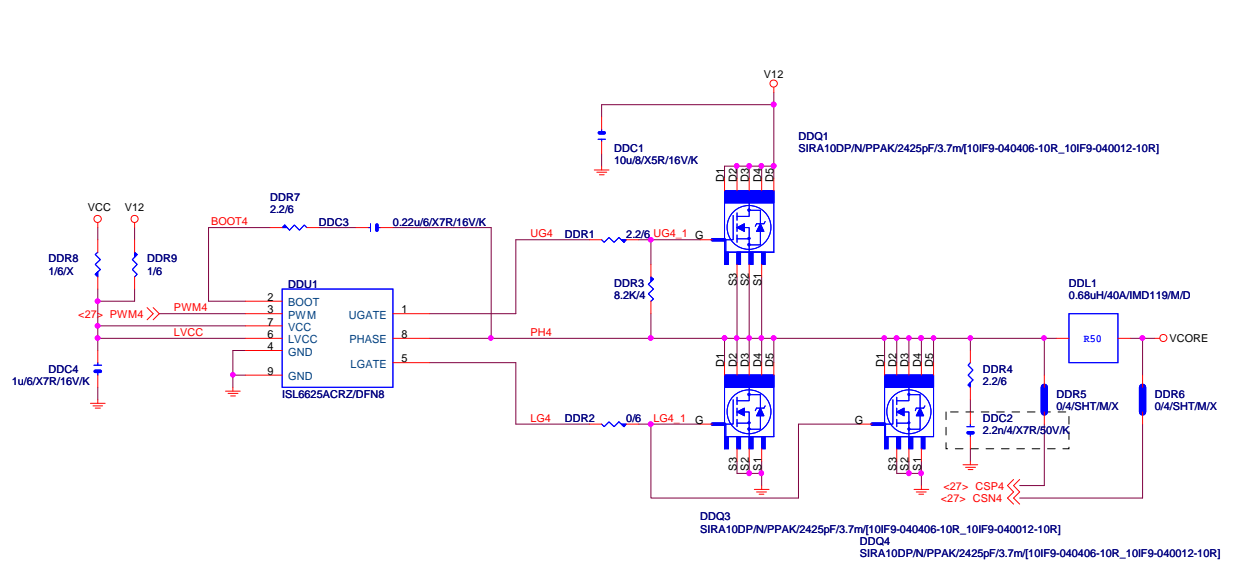
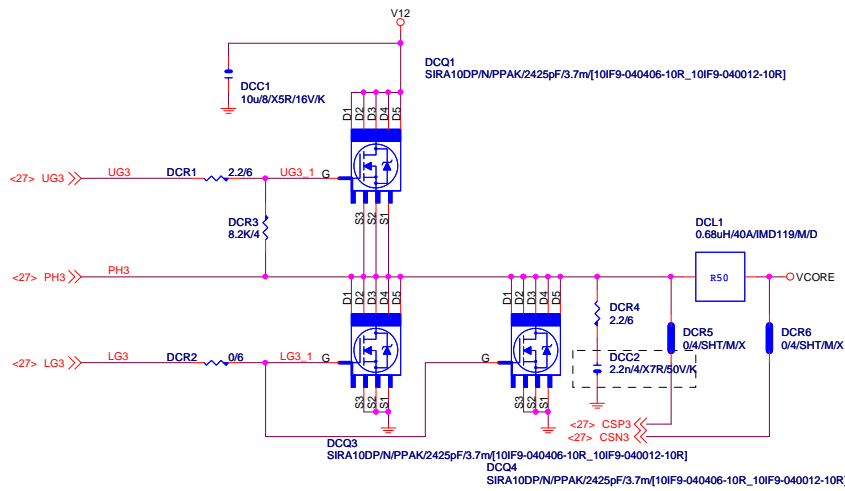
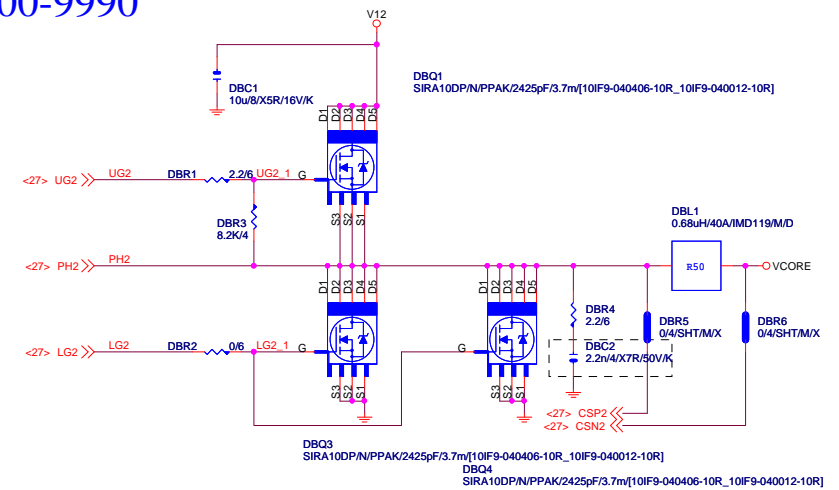
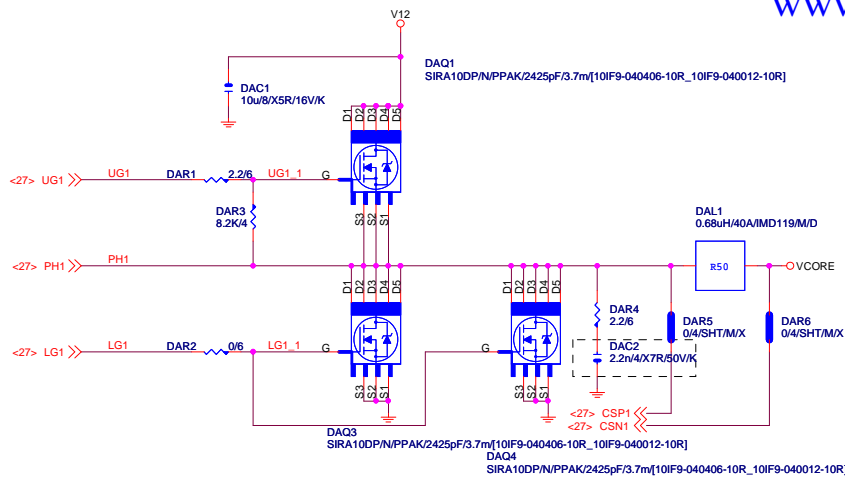
ATX CONNECTOR

GA-B85M-D3H

Rev 1.2

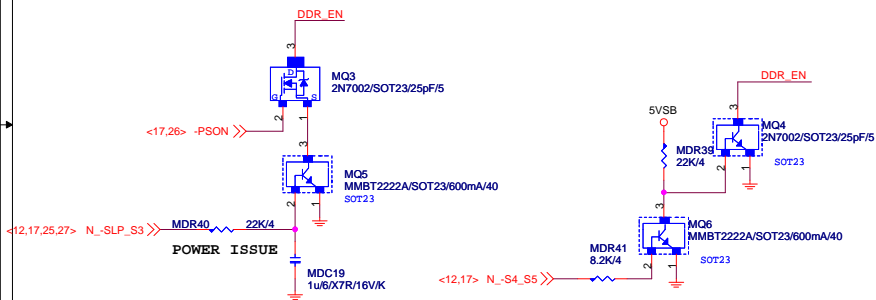
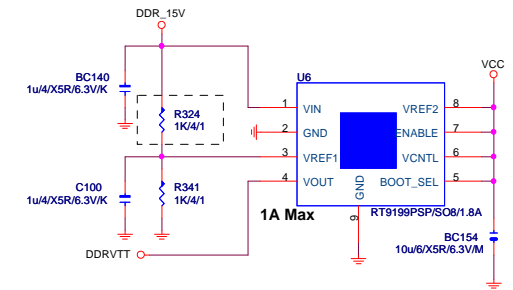
Title	Document Number	Rev
GA-B85M-D3H	GA-B85M-D3H	1.2
Date	Thursday, January 08, 2015	Sheet 26 of 32





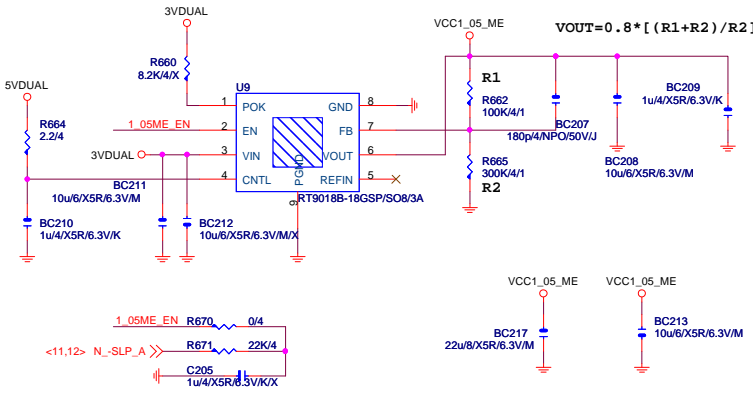
Gigabyte Technology

Title			
CPU CORE VR-2			
Size	Document Number	GA-B85M-D3H	
Custom		Rev	1.2
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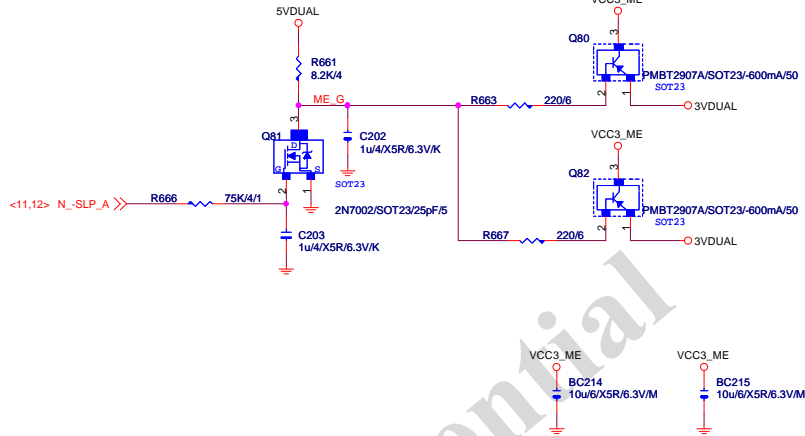


```
Rocset=(Iocp*Lgate,rdson)/Iocset
Rocset=(45A*6.7mOhm)/10uA = 30K
Iocset=10uA
```

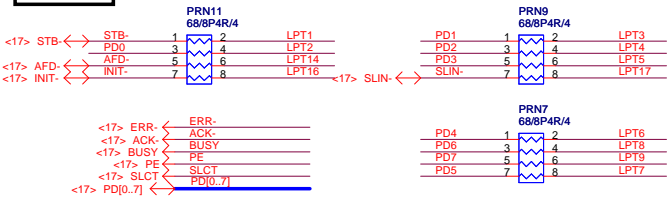
VCC1_05_ME



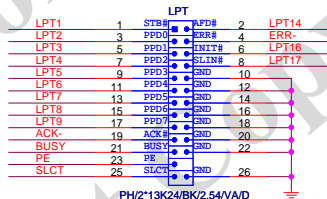
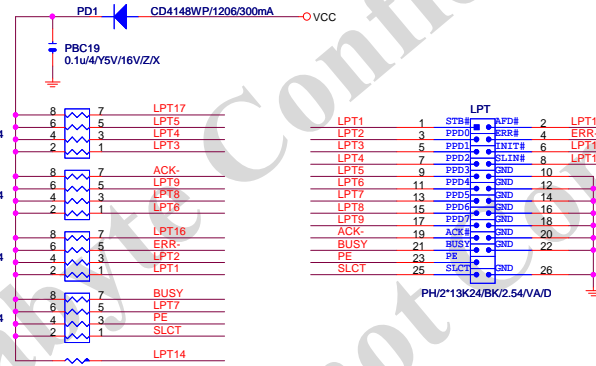
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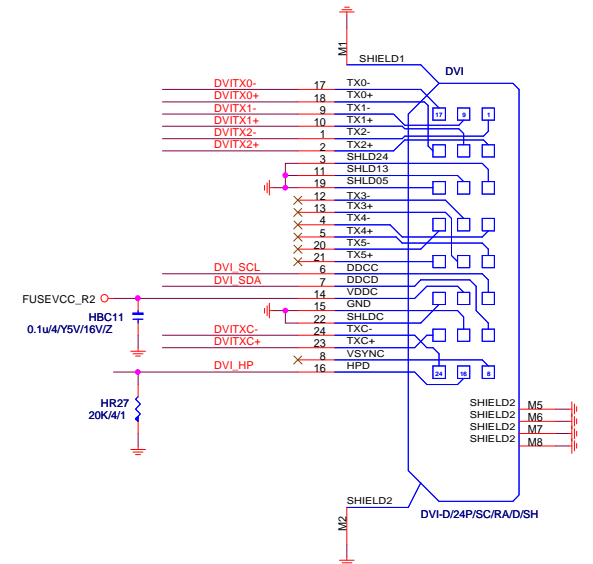
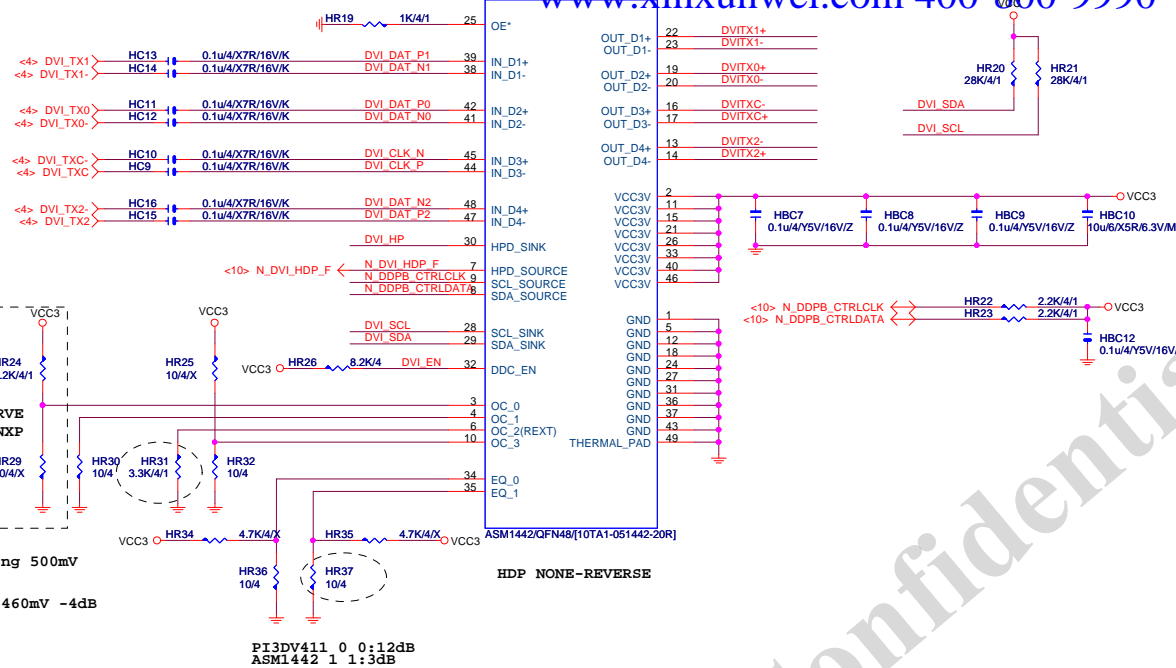
LPT PORT



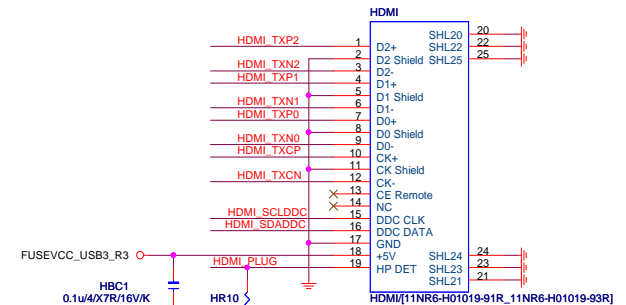
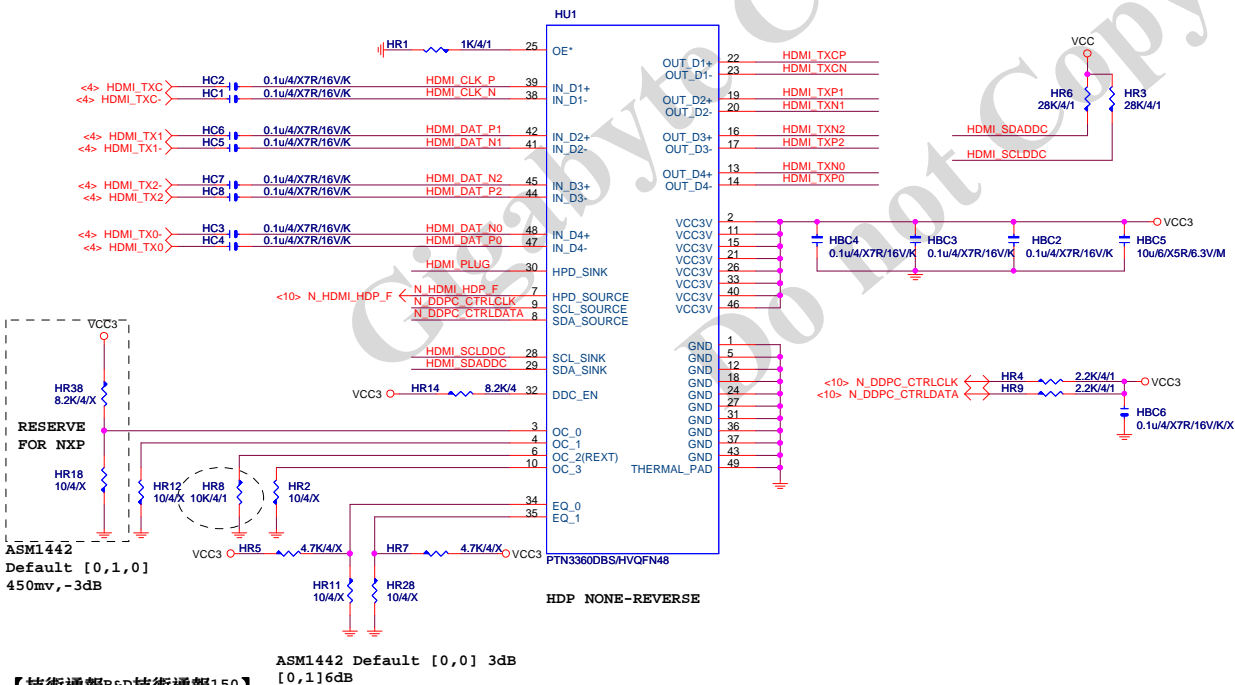
【技術通報R&D技術通報151】
33ohm Change to 68ohm



DVI LEVEL SHIFT



HDMI LEVEL SHIFT



【技術通報R&D技術通報150】

HDMI eye diagram 1.4版(deep color)會fail

原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram

改善: ASMedia ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

Gigabyte Technology

Title		
DVI		
Size Custom	Document Number	Rev
	GA-B85M-D3H	1.2
Date:	Thursday, January 08, 2015	Sheet 31 of 32

PCI:5/4/5 Impedance=50 +- 15%

BA_D[0..31] <-> BA_D[0..31] <16>

BC_BE0 <-> BC_BE0 <16>
BC_BE1 <-> BC_BE1 <16>
BC_BE2 <-> BC_BE2 <16>
BC_BE3 <-> BC_BE3 <16>

BPERR <-> BPERR <16>
BSERR <-> BSERR <16>

BPAR <-> BPAR <16>
BPLOCK <-> BPLOCK <16>
BDEVSEL <-> BDEVSEL <16>
BSTOP <-> BSTOP <16>
BTRDY <-> BTRDY <16>
BIRDY <-> BIRDY <16>
BFRAME <-> BFRAME <16>

PCIE_RST <-> PCIE_RST <14,15,17>

BPCIRST <-> BPCIRST <16>

BREQ0 <-> BREQ0 <16>
BREQ1 <-> BREQ1 <16>
BGNT0 <-> BGNT0 <16>
BGNT1 <-> BGNT1 <16>

BPCIPME1 <-> BPCIPME1 <16>



High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz



High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip

Co-Lay IT8893 (IT8893 CLKOUT1 N/A)

IT8892: PR24 -> 47ohm
IT8893: PR24 -> 22ohm

PR24 47/4/1 CLKOUT0
PR46 22/4/X
PR19 47/4/1 CLKOUT1

IT8892: PR19 -> O
IT8893: PR19 -> X

IT8892: PR19 -> O
IT8893: PR19 -> X

RREF PR13 12K/4/1

TEST_EN PR21 10K/4/1

EXT_ARB PR22 10K/4/1

RST_SEL PR7 10K/4/1

<10> G_PBCLK

PBC61 0.1u/4/X7R/16V/K G_PCIEBOP C
PBC62 0.1u/4/X7R/16V/K G_PCIEBON C
PBC43 0.1u/4/X7R/16V/K G_PCIEBIN C
PBC44 0.1u/4/X7R/16V/K G_PCIEBIP C

IT8892

PCI slot

PCI slot

chipset side

PRN14 0/8P4R/0402/SHT/X
BPIROA 1 2 <-> BPIROA1 <16>
BPIROD 3 4 <-> BPIROD1 <16>
BPIROB 5 6 <-> BPIROB1 <16>
BPIROC 7 8 <-> BPIROC1 <16>

VCCP PR26 0/4/SHT/M/X 3VDUAL

BPCIPME1 PR27 0/4/SHT/M/X <-> N_PCIE_WAKE <12,14,15,24>

PR2 0/4/SHT/M/X 3VDUAL
PCIEWAKE PR34 10K/4/1
BPCIPME PR43 10K/4/1

PRN1 2.2K/8P4R/4
BFRAME 1 2
BTRDY 3 4
BSTOP 5 6
BDEVSEL 7 8

PRN2 2.2K/8P4R/4
BIRDY 1 2
BPERR 3 4
BSERR 5 6
BPLOCK 7 8

PRN15 2.2K/8P4R/4
BPIROC 1 2
BPIROB 3 4
BPIROD 5 6
BPIROA 7 8

PRN4 2.2K/8P4R/4
BGNT3 1 2
BREQ3 3 4
BGNT1 5 6
BREQ1 7 8

PRN5 2.2K/8P4R/4
BGNT2 1 2
BREQ2 3 4
BGNT0 5 6
BREQ0 7 8

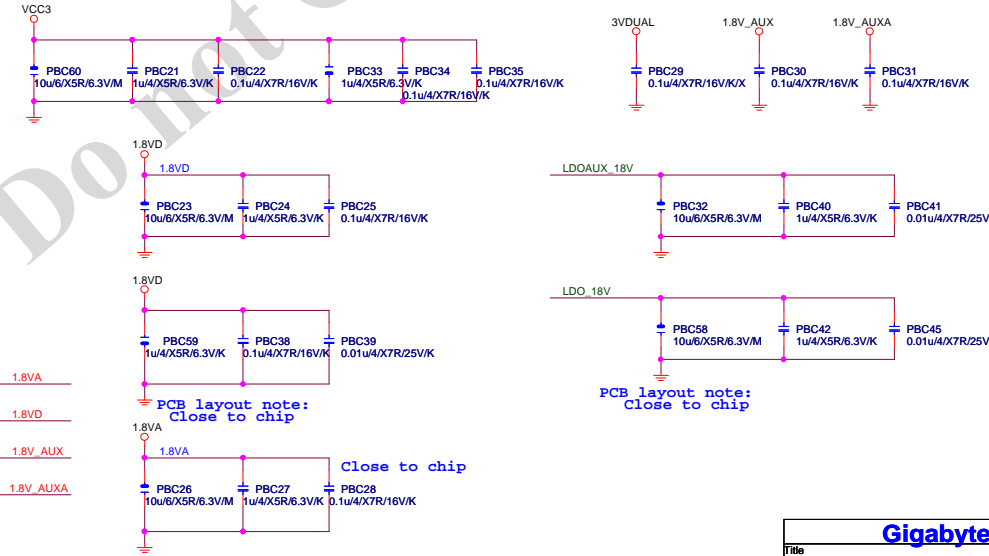
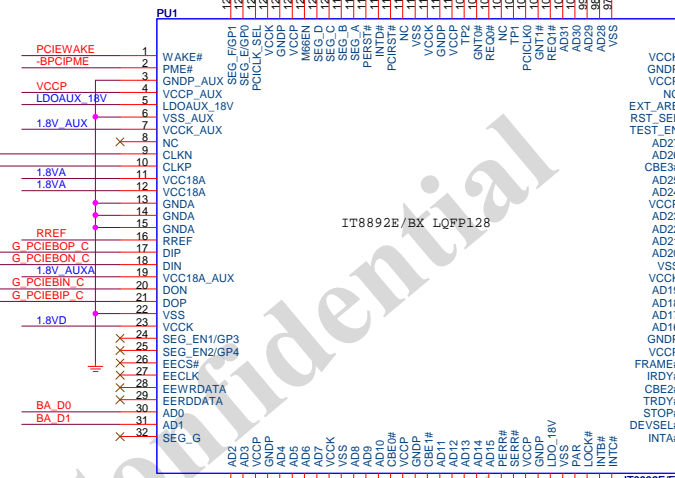
BPAR PR41 2.2K/4/1/X

LDO_18V PFB1 0/6/SHT/M/X 1.8VA

LDO_18V PFB2 0/6/SHT/M/X 1.8VD

LDOAUX_18V PFB3 0/6/SHT/M/X 1.8V_AUX

LDOAUX_18V PFB4 0/6/SHT/M/X 1.8V_AUXA

PCB layout note:
Close to chip

Close to chip

PCB layout note:
close to chip

Gigabyte Technology

Title			
ITE IT8892E			
GA-B85M-D3H			
Size	Document Number	Rev	1.2
Custom			
Date: Thursday, January 08, 2015			
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